DIRECT GROWTH OF Hg_{1-x}Cd_xTe BY COLD WALL, PYROLYTIC OMVPE

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ABSTRACT

Direct Growth of Hg_{1-x}Cd_xTe by Cold Wall, Pyrolytic OMVPE

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Mercury Cadmium Telluride ($Hg_{l-x}Cd_xTe$) is an important semiconductor alloy for infrared detector applications, because its band gap (-0.16 eV - 1.6 eV at 4.2 K) is a function of Hg to Cd ratio. The fabrication of staring arrays for infrared radiation detection with uniform wavelength sensitivity requires the large area thickness and compositionally uniform $Hg_{l-x}Cd_xTe$ alloys with excellent electronic properties.

Low temperature vapor phase epitaxy growth techniques have been proposed as a preferred approach for thick and thin film single crystal $Hg_{1-x}Cd_xTe$ synthesis. Thus, we have attempted here, for the first time the direct growth of $Hg_{1-x}Cd_xTe$ alloys by cold wall, pyrolytic OMVPE, using a newly designed ARIIV reactor that increases Hg incorporation, imposes uniformity, and avoids the need for high temperature processing to create alloys by interdiffusion approach.

The reactor was characterized in terms of the measured properties of CdTe, HgTe, and Hg_{1-x}Cd_xTe epilayers grown in it. The growth rate of CdTe, HgTe, and Hg_{1-x}Cd_xTe were correlated with substrate temperature, reactant flow rates (DMCd, MATe and elemental Hg) and reactor design. The growth rate of the Hg₁₋ _xCd_xTe layers at T_s=300 °C, which was experimentally optimized condition, decreased from 4 to 0.75 µm/h, but the composition of the Hg_{1-x}Cd_xTe epilayers increased from x=0.126 to x=0.359 as the DMCd flow rate increased from 0.0075 to 0.06 sccm at fixed MATe flow rate of 1.55 sccm. Thickness and compositional mapping of thin $Hg_{l-x}Cd_xTe$ epilayers, by employing the multiple angle of incidence ellipsometry technique, showed excellent uniformity with 990 ± 20 Å for thickness uniformity and $x = 0.602 \pm$ 0.002 for compositional uniformity. The compositional uniformity of a 4 µm thick $Hg_{l-x}Cd_xTe$ epilayer (x = 0.152) also revealed exceptional uniformity, with $\Delta x = \pm$ 0.002 over a 6.45 cm² area of the CdTe substrate.

The Hall effect measurements on as-grown $Hg_{l-x}Cd_xTe$ showed *n*-type characteristics for x<0.25 and *p*-type characteristics for $x\ge0.25$. The electron carrier concentrations and the electron mobilities for *n*-type $Hg_{l-x}Cd_xTe$ materials were in the range (0.4 - 2.6) x 10¹⁶ cm⁻³ and (2.0 - 4.1) x 10⁴ cm²/V·sec, respectively, at 77 K. The relatively constant carrier concentration and rather poor mobilities as a function of temperature was determined to be subgrain boundaries in the CdTe substrates.

List of Acronyms

ARI:	Annular Reactant Inlet
ARIIV:	Annular Reactant Inlet Inverted-Vertical
CCD:	Charged Coupled Device
CdTe:	Cadmium Telluride
CdZnTe:	Cadmium Zinc Telluride
CID:	Charge Injection Device
DAG:	Direct Alloy Growth
DCXRC:	Double Crystal X-ray Diffraction
DETe:	Diethyltelluride
DIPTe:	Diisopropyltelluride
DMCd:	Dimethylcadmium
DPTe:	Dipropyltelluride
DTBTe:	Ditertiarybutyltelluride
ECCP:	Electron Contrast Channeling Patterns
ESCA:	Electron Spectroscopy for Chemical Analysis
FPA:	Focal Plane Array
FTIR:	Fourier Transform Infrared
FWHM:	Full Width at Half Maximum
GaAs:	Gallium Arsenide
HgTe:	Mercury Telluride
$Hg_{1-x}Cd_xTe$:	Mercury Cadmium Telluride
ID:	Inside Diameter
I _{light} :	Intensity of light
IMP:	Interdiffused Multi-layer Process

InSb:	Indium Antimonide
IR:	Infrared
ISOVPE:	Isothermal Vapor Phase Epitaxy
IV:	Inverted-Vertical
LPE:	Liquid Phase Epitaxy
LWIR:	Long Wavelength Infrared (8-14 $\mu m)$
MAI:	Multiple Angle of Incidence
MATe:	Methylallyltelluride
MBE:	Molecular Beam Epitaxy
MFC:	Mass Flow Controller
μm/h:	Epilayer Growth Rate per Hour
MOCVD:	Metal Organic Vapor Phase Epitaxy
MOSFET:	Metal-Oxide-Semiconductor Field-Effect Transistor
MWIR:	Middle Wavelength Infrared (3-5 μm)
OD:	Outside Diameter
OMVPE:	Organometallic Vapor Phase Epitaxy
<i>Q</i> :	Flow Rate
Q_{Cd} :	Flow Rate of DMCd
Q_{Te} :	Flow Rate of MATe
<i>R_g</i> :	Growth Rate
SCCM:	Standard Cubic Centimeter per Minute
SEM:	Scanning Electron Microscopy
SLPM:	Standard Liters Per Minute
SSR:	Solid State Recrystallization
TCE:	Trichloroethane
THM:	Traveling Heater Method
T _s :	Substrate Temperature

_

VPE:

1. INTRODUCTION

Mercury cadmium telluride ($Hg_{1-x}Cd_xTe$) is an important semiconductor alloy for infrared detector applications, because its bandgap (-0.16 to 1.6 eV, at 4.2 K) is a function of Hg to Cd ratio as shown in Fig. 1.1. $Hg_{1-x}Cd_xTe$ sensitivity covers the entire spectral range from 0.8 µm (CdTe) to over 20 µm. This range is ideally suited for infrared detectors because the two atmospheric transmission windows for infrared wavelengths are at middle wavelength infrared (MWIR: 3-5 µm) and at long wavelength infrared (LWIR: 8-14 µm) [1 - 4]. The energy gaps which can detect infrared signals in these wavelength ranges are 0.25 - 0.4 eV for MWIR signals and 0.1-0.15 eV for LWIR signals. The $Hg_{1-x}Cd_xTe$ composition of special importance for detectors in the 8-14 µm range, where it has unrivaled performance, is $x \equiv 0.2$.

Hg_{1-x}Cd_xTe photodiodes, as detectors for infrared radiation, are conventionally fabricated from high purity *p*-type material, whereas photoconductors are fabricated from high purity *n*-type material. Although the photodiode device is much more difficult to fabricate, due to the requirement for a *p*-*n* junction, it can be used in the form of large focal plane arrays (FPA). These arrays, when coupled to charge coupled devices (CCD's), charge injection devices (CID's), or MOSFET x-y addresses, allow for a more efficient detector system compared to the photoconductors because of the lower power [5].

The fabrication of a large number of IR radiation detector elements (metalinsulator-semiconductor photodiodes or photoconductors) in array form depends on an ability to obtain a thin layer of this material with excellent electronic properties and uniformity. The uniformity requirement is difficult to meet by traditional bulk crystal methods such as Bridgeman technique [6-8], solid state



WAVELENGTH (µm)

Figure 1.1 Infrared detectors as a function of wavelength.

N

1

recrystallization (SSR) [9], and traveling heater method (THM) [10]. Epitaxial growth techniques have been utilized as an alternative for this reason.

In the past two decades, the development of Hg_{1-x}Cd_xTe for IR detector focal plane arrays has emphasized methods of thin-film epitaxial growth such as liquid phase epitaxy (LPE) [11], isothermal vapor phase epitaxy (ISOVPE) [12], organometallic vapor phase epitaxy (OMVPE) [13], and molecular beam epitaxy (MBE), because of the known and accepted advantages of such configurations. These advantages include (1) the possible use of commercially available existing substrate materials; (2) growth of two or more epitaxial layers in sequence on the same substrate allows the preparation of complete detector structures, including heterojunctions, in a single growth process or series of growth processes; (3) epitaxial layers made by these growth techniques can be more uniform in composition, compared to bulk $Hg_{l-x}Cd_xTe$; and (4) the use of high-resistivity (semi-insulating) CdTe or other wide bandgap semiconductors as substrate material allows backside illumination of the detector, through the substrate, so that the required contacts to the individual detectors of an array can be made from the top side by flip-chip or other suitable bonding techniques without interfering with the array optics.

The LPE process has three important limitations: (1) LPE can only be employed to grow $Hg_{l-x}Cd_xTe$ epilayers on substrates that are thermally and chemically stable in a Hg or Te solvent; (2) LPE grown epilayers have graded compositions in the direction of growth due to the high temperatures employed by the process; and (3) impurities used to increase the resistivity of CdTe and (CdZn)Te substrates diffuse into LPE grown $Hg_{l-x}Cd_xTe$ epilayers during growth, thereby causing unpredictable changes in the conductivity of both the substrate and the epilayer. Low temperature vapor phase epitaxy (VPE) is a preferred approach for thick and thin film single crystal $Hg_{l-x}Cd_xTe$ synthesis. The advantages of this VPE film approach are (1) substrates other than CdTe (also difficult to grow) can be used, e.g. InSb; and (2) the electrical and compositional uniformity of epilayers is generally far superior to bulk material. Further, thick in-situ CdTe buffer layers can be grown on cheaper, more readily available substrates, and the $Hg_{l-x}Cd_xTe$ epilayers can subsequently be grown on the buffer layers; then the initial substrate can be removed from the buffer and $Hg_{l-x}Cd_xTe$ layers.

Organometallic vapor phase epitaxy (OMVPE) would be the preferred VPE approach for epitaxial growth of Hg_{1-x}Cd_xTe epilayers (0 μ m < thickness $\leq 100 \mu$ m), if the Hg to Cd ratio could be easily controlled. A major advantage of OMVPE is low temperature synthesis ($\leq 300 \text{ °C}$); thus, better uniformity can be obtained through the layer, lower vacancy concentration can be obtained in the layer, and any suitable substrate can be used. Thus, we have attempted here, for the first time the direct growth of Hg_{1-x}Cd_xTe compounds by cold wall, pyrolytic OMVPE, using the metalorganic reactants dimethylcadmium [(CH₃)₂Cd : DMCd] and methylallyltelluride [C₄H₈Te : MATe], and a newly designed annular reactant inlet inverted vertical (ARIIV) reactor that increases Hg incorporation, imposes uniformity, and avoids the need for high temperature processing to creat alloys by interdiffusion approach.

The overall technical approach to CdTe, HgTe, and Hg_{1-x}Cd_xTe growth is described in the next chapter. Chapter 3 presents a detailed technical approach used for this study. The procedures of epilayer growth and characterization are described in Chapter 4. The characterization results of as-grown II-VI epilayers are discussed in Chapter 5, followed by conclusions in Chapter 6.

2. BACKGROUND OF Hg_{1-x}Cd_xTe

2.1 Physical Properties

Hg_{1-x}Cd_xTe has been extensively studied since Lawson et al. reported that the ternary alloy could be synthesized from HgTe and CdTe [14]. The energy gap of Hg_{1-x}Cd_xTe varies between $E_g = 1.6 \text{ eV}$ (CdTe) and -0.3 eV (HgTe) at 4.2 K. The energy gap of Hg_{1-x}Cd_xTe is direct and increases linearly from -0.3 eV to 1.6 eV as x increases from 0 (HgTe) to 1 (CdTe) as shown in Fig. 2.1. The Γ_6 state, which is the conduction band minimum in CdTe and other zinc-blende semiconductors, lies at a lower energy in HgTe than the Γ_8 state, which is the valence band maximum in CdTe. Thus the energy gap E_0 is negative in HgTe. The usual light-hole valence band becomes the conduction band and the conduction band and portion of Figure 2.1 illustrates the region near the Γ point when the energy gap is relatively wide, where it is parabolic.

The composition dependence of energy gap, the $\Gamma_6 - \Gamma_8$ transition, at 0 and 300 K is illustrated in Fig. 2.1. This figure shows the temperature coefficient $[= \partial E_g/\partial T]$ of the energy gap for CdTe is negative, which is usual for most semiconductors, but is positive for Hg rich compositions. The compositional and temperature dependencies for x values equal to or less than 0.40, which is the region of most interest in IR detector fabrication, have been extensively studied to measure the composition or the x value at a given temperature without destroying the Hg_{1-x}Cd_xTe. The two primary optical properties of interest in epitaxial Hg_{1-x}Cd_xTe layers are absolute percent transmittance and spectral cut-onwavelength and shape. The spectral cut-on as determined using IR spectroscopy is a measure



Figure 2.1 Energy gap as a function of composition in $Hg_{1-x}Cd_xTe$ [16].

of the x value of the $Hg_{l-x}Cd_xTe$ at a given temperature. A revised empirical relation in the composition range of $x \le 0.6$ and x = 1 was proposed by Hansen *et al.* [17].

$$E_{g} = 1.24/\lambda_{c} (\mu m) = -0.302 + 1.93x + 5.35(10^{-4})T(1-2x)$$
$$-0.801x^{2} + 0.832x^{3} \dots Eq. 2.1$$

where E_g is the energy gap in electron volts, x is the composition variable, and T is the absolute temperature. The composition of $Hg_{1-x}Cd_xTe$ is measured by using Equation 2.1 if the cut-on wavelength and the measuring temperature are known.

The thickness of $Hg_{l-x}Cd_xTe$ epitaxial layer is measured by the interference pattern observed on the IR spectral transmission curves. The periodicity of the interference pattern is directly related to layer thickness, which is given by expression,

d (
$$\mu$$
m) = 10⁴/2n(Δ v), Eq. 2.2

where d is the layer thickness in μ m, n is refractive index of layer, and Δv is average separation per cycle (i.e., distance between successive peaks) from the interference pattern in wave numbers. The values for the refractive indices of Hg_{1-x}Cd_xTe are available from the reference *Properties of Mercury Cadmium Telluride* [18].

The structure of $Hg_{1-x}Cd_xTe$ is the zinc-blende, which is composed of four atoms in the unit cell, as shown in Figure 2.2. This figure shows that the cations (Hg or Cd) are distributed randomly over the cation sites [(0 0 0), (0 1/2 1/2), (1/2 0 1/2), (1/2 1/2 0)] and the anion (Te) occupies the other sites [(1/4 1/4 1/4), (1/4 3/4 3/4), (3/4 1/4 3/4) (3/4 3/4 1/4)]. The lattice constant of HgTe is 6.463 Å (x=0)



Figure 2.2 Crystal structure of $Hg_{1-x}Cd_xTe$; A site: Cd or Hg and B site: Te [19].

and that of CdTe (x=1) is 6.482 Å at room temperature.

The dependence of lattice constant and density is shown in Figure 2.3. The lattice constant variation as a function of composition shows a small deviation from Vegard's law, which is linear with composition, but the variation of density is linear with composition. Because of this linearity of density with composition, density measurement of $Hg_{l-x}Cd_xTe$ is often used in determining the composition indirectly by weighing the substrate before and after epitaxial film deposition. The lattice mismatch between CdTe and $Hg_{0.8}Cd_{0.2}Te$ is about 0.3% [21,22]. It is natural that CdTe is chosen as a substrate of $Hg_{l-x}Cd_xTe$ epitaxial films because of this small lattice mismatch.

The electrical properties of $Hg_{l-x}Cd_xTe$ could be indirectly obtained by measuring the Hall coefficient R_{H} . The drift currents carried by both holes and electrons add to give a larger total if both are present in the same material. The current density J carried by drifting carriers is

$$J = nqv_d$$
, Eq. 2.3

where n is the number of carriers of charge q per unit volume and v_d is the drifty velocity; if there are different kinds of carriers present they contribute separately to the current density.

The electrical conductivity σ of a sample is defined as the current that flows across unit cube when the field and hence voltage between opposite faces is unity. Thus for a semiconductor where both electrons and holes are present,

$$\sigma = ne\mu_e + pe\mu_h \cdots Eq. 2.4$$



Figure 2.3 Lattice constant and density versus alloy composition x in Hg_{1-x}Cd_xTe [20].

where n is the electron carrier concentration, p is the hole carrier concentration, *e* is the charge 1.6 x 10^{-19} C, and μ_e and μ_h are the mobilities of electrons and holes, respectively. When a current of density J flows in a semiconductor perpendicular to a magnetic field **B**, there is a force on the electrons and holes perpendicular to both **B** and J, and a transverse current tends to flow. The process is known as the *Hall effect*, and provides a way of estimating the majority-carrier density and sign, and is also the basis of devices for measuring magnetic field and for multiplying two signals. The Hall coefficient depends on the applied current and voltage and the intensity of applied magnetic field.

For p-type semiconductor, in case of weak magnetic field, the Hall coefficient $R_{\rm H}$ is

$$R_{\rm H} = (r/ep) \, \text{S},$$
 Eq. 2.5

where r is scattering factor, which includes the lattice scattering and the ionized impurity scattering, and S is the symmetry factor [23].

S is also described by the following formula using the ratio K, the longitudinal effective mass to the transverse effective mass,

$$S = 3K (K+2)/(2K+1)^2$$
, Eq. 2.6

where K depends on the band structure of materials. When the band structure of a material is parabolic, S becomes 1 because K has the value of 1 at k = 0. The generalized formula for electrons and holes, from Equation 2.5, becomes

$$R_{\rm H} = (R_{\rm n}\sigma_{\rm n}^2 + R_{\rm p}\sigma_{\rm p}^2)/(\sigma_{\rm n} + \sigma_{\rm p})^2$$
. Eq. 2.7

If we consider the scattering factor r and the symmetry factor S=1 and use Equation 2.4, the Hall coefficient is described as follows,

$$R_{\rm H} = r(p - nb^2)/e(p + nb)^2$$
, Eq. 2.8

where b is the mobility ratio of electrons to holes. Because the scattering factor r is approximately unity for $Hg_{1-x}Cd_xTe$ [23, 24], the Hall coefficient of $Hg_{1-x}Cd_xTe$ can be described by

$$R_{\rm H} = (p - nb^2)/e(p + nb)^2$$
. Eq. 2.9

The temperature dependence of the Hall coefficients of n and p-type Hg₁. $_{x}Cd_{x}Te$, which is a narrow energy gap semiconductor, show totally different characteristics according to Capper's model [25]. Unlike n-type Hg_{1-x}Cd_xTe, for which no evidence of donor freeze out has been reported, p-type $Hg_{l-x}Cd_xTe$ generally shows appreciable acceptor freeze out at low temperatures. Lou et al. [26] classified p-type $Hg_{l-x}Cd_xTe$ into two cases, i.e., anomalous phenomena and classical phenomena. The anomalous phenomena of p-type Hg_{1-x}Cd_xTe shows a dip or double inversion in the temperature dependence of the Hall coefficient with increasing temperature [27 - 29]. In case of classical phenomena, it is generally accepted that the temperature dependence of the Hall coefficient for *p*-type narrow energy gap semiconductors reveals the type transition [30], that is, a conversion from n- to p-type characteristics as the temperature decreases, as shown in Figure 2.4. From Figure 2.4, AB is the intrinsic region that the electron conduction is dominant, BCD is the mixed conduction region in which electrons and holes contribute to conduction, DE is the ionized acceptor region, and EF is the region of acceptor freeze out. The Hall coefficient in the region AB,





in terms of intrinsic carrier concentrations ni, is described by

$$R_{\rm H} = -1/en_{\rm i}$$
, Eq. 2. 10

where n_i is a function of temperature T and composition x [31], which is expressed by

$$n_i (x, T) = (5.585 - 3.820x + 0.001753T - 0.001364xT) \cdot 10^{14}$$

 $\cdot E_g^{3/4}T^{3/2}exp (-E_g/2kT).$ Eq. 2.11

The Hall coefficient of both electrons and holes, in region BCD, is

$$R_{\rm H} = (p-nb^2)/e(p+nb)^2$$
. Eq. 2.12

In region DEF, which acceptors are dominant, the Hall coefficient is described as

$$R_{\rm H} = 1/e{\rm p}.$$
 Eq. 2.13

In determining the electrical transport properties of $Hg_{1-x}Cd_xTe$ by Hall measurement, anomalous effects can sometimes occur and lead to results which are difficult to interpret. The main reasons for this anomalous behavior are the large ratio of electron mobility to hole mobility as well as the tendency to form *n*-type conducting regions either within the bulk or at the surface of the *p*-type $Hg_{1-x}Cd_xTe$ samples. Zemel *et al.* [32] studied this problem by investigating the magnetic field dependence of the Hall coefficient in association with a layer model. Accurate results on concentration and mobility of both electrons and holes can be obtained.

The electrical properties and the controllability of these properties in epitaxially grown materials are as important as the crystallographic properties in fabricating electrical devices. The most important factor to be considered prior to the application of a certain material is the doping characteristics.

There are basically two methods for doping $Hg_{l-x}Cd_xTe$ semiconductors, one using intrinsic defects of the material by controlling stoichiometry and the other using extrinsic dopants by substituting foreign elements with different numbers of valence electrons. To help understand point defects (intrinsic defects) in $Hg_{l-x}Cd_xTe$ crystals, which have important effects on the electrical properties of $Hg_{l-x}Cd_xTe$ epitaxial or bulk crystals, these defects will be discussed in some detail.

There are various types of point defects in $Hg_{I-x}Cd_xTe$ crystals. Major point defects are vacancies (V_{Hg} and V_{Te}), interstitials (Hg_I and Te_I), antisites (Te_{Hg} and Hg_{Te}), and foreign elements (interstitial and substitutional).

Among these defects, the Hg vacancy is the most abundant defect at the processing temperatures of most growth techniques, due to the high equilibrium vapor pressure of Hg and the retrograde solubility of Te (specially for Hg_{*l*-*x*}Cd_{*x*}Te prepared at higher temperature). In most cases, Hg_{*l*-*x*}Cd_{*x*}Te is grown under Te-rich conditions to avoid the high Hg pressure of the Hg-rich process. If the Hg_{*l*-*x*}Cd_{*x*}Te crystal (bulk or epitaxial film grown at temperatures higher than 400 °C) is rapidly cooled to room temperature, there will be excess Te and Hg vacancies due to the higher solubility of Te at the growth temperature. This excess Te causes precipitates under low temperature annealing conditions, if it is not properly annihilated. Also the Hg vacancy is the most important defect related to the electrical properties of the material. Hg vacancies in Hg_{*l*-*x*}Cd_{*x*}Te are responsible for the observed shallow acceptor level; thus they will impact the electron and hole mobility. They are also responsible for dislocation formation and propagation as well as self diffusion and impurity diffusion.

The majority of undoped bulk and epitaxial (LPE or ISOVPE) $Hg_{l-x}Cd_xTe$ alloys grown at higher temperature ($\geq 400 \text{ °C}$) exhibit p-type characteristics and the origin of this *p*-type behavior is generally believed to be due to the existence of doubly ionized Hg vacancies [33].

The other physical properties of $\text{Hg}_{l-x}\text{Cd}_x\text{Te}$ which are proper to IR detection are: (1) the high absorption coefficient ($\alpha \cong 1,000 \text{ cm}^{-1}$), (2) low intrinsic carrier concentrations ($\cong 10^{14} - 10^{15} \text{ cm}^{-3}$) and high electron mobility ($\cong 10^4 - 10^5 \text{ cm}^2/\text{V}$ · sec) in *n*-type $\text{Hg}_{l-x}\text{Cd}_x\text{Te}$, and (3) a small radiative carrier life time ($\cong 0.1 - 10 \text{ }\mu\text{s}$) for *n*-type $\text{Hg}_{l-x}\text{Cd}_x\text{Te}$ [33]. The physical properties, including additional characteristics of $\text{Hg}_{l-x}\text{Cd}_x\text{Te}$, are summarized in Table 2.1 [18].

2.2 Crystal Growth Techniques of Hg_{1-x}Cd_xTe

2.2.1 Bulk Crystals

The liquid/solid quasi-equilibrium methods of crystal growth have always been considered for high quality semiconductor compounds due to their potentially better crystal structure. However, for a ternary alloy of $Hg_{l-x}Cd_xTe$, with a large difference in liquidus and solidus compositions along a given thermal tie line (Figure 2.5 [34]), the slow moving interface of these near equilibrium processes invariably result in compositional grading through the crystal.

Several modified bulk crystal growth methods such as slush growth, SSR growth [9], and THM [10, 13], have been used subsequent to the development of Bridgman growth by Wooley *et al.* [21], in order to find slices of excellent compositional uniformity at certain composition.

In the vertical Bridgman (Stockbarger) method of crystal growth, directional freezing results in a compositional variation along the growth axis [35]. Bartlett *et al.* [36, 37] reported that the compositional variation along the longitudinal and

	HgTe	Hg _{0.8} Cd _{0.2} Te	CdTe
Lattice structure	cubic (ZnS)	cubic (ZnS)	cubic (ZnS)
Lattice constant a (300 K) [Å]	6.463	6.4645	6.482
Density γ (300 K) [g·cm ⁻³]	8.076	7.63	5.85
Opt. phonons (77 K) LO (cm ⁻¹) TO (cm ⁻¹)	138 118	157 150	170 142
Energy gap (eV) $E_g (300 \text{ K})$ $E_g (77 \text{ K})$ $E_g (0 \text{ K})$	-0.15 -0.25 -0.30	0.165 0.09 0.064	1.505 1.60 1.605
Effective mass (77 K) M [*] _c /m _o M [*] _v /m _o	0.029 0.35 - 0.7	0.005 0.2 - 0.6	0.096 0.66
Mobilities (77 K) $\mu_n (cm^2/V \cdot sec)$ $\mu_p (cm^2/V \cdot sec)$		2.5 x 10 ⁵ 7 x 10 ²	
Intrinsic carr. conc. n _i (300 K) [cm ⁻³] n _i (77 K) [cm ⁻³]		2 x 10 ¹⁶ 2 x 10 ¹³	
Melting point (K)	943	1050	1365
Thermal exp. coeff. α (300 K) [10 ⁻⁶ K ⁻¹] α (77 K) [10 ⁻⁶ K ⁻¹]	4 2	4.3	5.5
Thermal conductivity C (300 K) [Wcm ⁻¹ K ⁻¹ s ⁻¹] C (77 K) [Wcm ⁻¹ K ⁻¹ s ⁻¹]	0.04 0.15		0.07 0.4

Table 2.1 Physical properties of $Hg_{l-x}Cd_xTe$ [18].



Figure 2.5 (T,x) phase diagram of Hg_{1-x}Cd_xTe [33].

radial directions depends upon the crystal growth rates. They also showed that mixing conditions in the melt (related to sizes of raw material charging the crucible, temperature gradient of growth furnace, and gravity) make it difficult to grow crystals free of compositional grading by the vertical Bridgman method.

Slush growth, a new method of taking advantage of the CdTe-HgTe pseudobinary alloys, was proposed by Harman [38]. In this method, a charge of alloy is prepared by conventional methods and lowered quickly to a new position in a vertical furnace so that the bottom part of the liquid is quickly frozen. Harman reported that a high quality single crystal is found in the solid region adjacent to the portion of the solid/liquid interface just prior to cooling. This slush growth method was adapted by Cominco for the production of bulk, detector grade Hg₁₋ $_x$ Cd_xTe. This method produces a very narrow homogeneous zone in a very short distance, when a steady state is reached.

The use of very high quenching rates [36] induces macroscopic defects such as coring or intergranular precipitates in the crystalline structure. The quenchanneal process can minimize the short-range segregation. Once quenched to a fine grained, reasonably homogeneous alloy free of voids and other mechanical problems, it is necessary to recrystallize into a more or less single-crystal boule from which useful slices for fabrication can be taken. This SSR growth increases the size of grains, but does not improve the long-range homogeneity.

To solve the difficult metallurgical problems of $Hg_{l-x}Cd_xTe$, which are the growth of homogeneous, pure, and large oriented crystals, the THM process has been developed [39-41]. This method, which can be considered as a continuous LPE process, allows a crystal to reach a steady state, to benefit from the advantages of low temperature growth, to have its charge purified, and to easily use an oriented seed. In this process, a molten zone is made to migrate through a solid, homogeneous source material by the slow movement of the ampoule relative

to the heater. Matter transport occurs by convection and diffusion across the solvent zone under the influence of the temperature gradient resulting from the movement. THM is known as the most effective technique for achieving material of electronic device grade, i.e., for IR detector arrays, among the bulk crystal growth methods of $Hg_{1-x}Cd_xTe$. Although THM has some strong advantages, it cannot produce single crystals larger than 1 inch in diameter within a reasonable amount of time and with a reasonable cost.

2.2.2 Epitaxial Layers

Bulk $Hg_{J-x}Cd_xTe$ crystals grown by different techniques are usually used in the simple detector fabrication of photoconductive or photovoltaic devices. The highest performance of modern devices, however, often requires more sophisticated structures that can only be achieved by epitaxial growth techniques. Epitaxial techniques offer important advantages in comparison with bulk growth, i.e., lower temperatures and mercury vapor pressures, shorter growth times, and reduced precipitation problems which enable the growth of large-area samples with good lateral homogeneity. These advantages make it possible to grow multiple layer structures with sharp interfaces, superlattice, and quantum well structures for possible control of long wavelength tunneling currents. The above advantages have prompted research in a variety of epitaxial techniques, including LPE [11], VPE [12], OMVPE [13], and MBE [42 - 46].

 $Hg_{l-x}Cd_xTe$ epitaxial layers are generally grown on lattice matched CdTe or $Cd_{l-x}Zn_xTe$ (x=0.04 - 0.06) substrates. Because of the difficulties encountered in obtaining high quality, large area substrates, several laboratories initiated projects that sought to synthesize an alternate substrate for $Hg_{l-x}Cd_xTe$. Cost considerations and the possibility of large-scale production have stimulated interest in hybrid structures for epitaxy, e.g. CdTe epitaxial layers on
commercially available GaAs substrates [42]. The CdTe buffer layer forms a transition region between the substrates and the active layers used for the devices.

At present, LPE is the most mature method of device-quality layer fabrication. Due to the relatively low cost and the simplicity with which a growth apparatus can be assembled on a laboratory scale, LPE has received the most attention. Individualized techniques differ in respect to the melt composition, the methods of bringing the substrate into contact with the melt, and the growth mode. Both Hg and Te are suitable solvents in the LPE growth technique. The major difference between growth in Hg- and Te-rich liquids is that the Hg vapor pressure over the melt is much higher in the former case. LPE growth from Te-rich solution has been carried out more extensively than that from Hg-rich liquids not only because of lower Hg vapor pressure, but also because of the relatively high solubility of Hg and Cd elements in the Te liquid at low temperatures. The partial pressure of Hg must be controlled in the growth system in order to obtain compositional uniformity, reproducibility and stability.

The use of Hg as a solvent has certain advantages: Hg is the purest metal that is readily available, and both n- and p-type layers with low net doping levels and little deviation from stoichiometry can be grown. The low solubility of CdTe makes the growth rate low and is the reason for melt depletion during growth. This results in composition changes as growth proceeds. The problem can be overcome by the use of large melt mass, which can result in excellent composition uniformity of layers from run to run.

Difficulties in extending best laboratory results for LPE to production scale are well known for $Hg_{l-x}Cd_xTe$ epilayers [47]. The relatively poor morphology and the difficulties in controlling composition gradients and establishing true heterojunctions in this relatively high temperature growth technique, makes the outlook for reproducible growth of long wavelength photovoltaic arrays uncertain. ISOVPE for $Hg_{1-x}Cd_xTe$ crystals was developed in the mid 1960s [48, 49]. In this method, substrate and source are usually placed in parallel to each other in a closed ampoule and then the temperature is elevated to between 450 °C and 650 °C. The growth proceeds because the dissociation partial pressures of Hg and Te over HgTe exceed the equilibrium values of $Hg_{1-x}Cd_xTe$. During the deposition process, interdiffusion of deposited HgTe and CdTe occurs, resulting in growth of graded gap $Hg_{1-x}Cd_xTe$ layers. The composition profile depends on temperature and time of deposition, the Hg pressure and on the system geometry. The optimum growth conditions are reached when the temperature of the growth zone and the pressure of Hg closely approach the Te-rich boundary line.

MBE is well established as a powerful method for growing thin films, quantum well structures, and superlattices. MBE is a material growth technique in which crystalline epitaxial films are grown by depositing atoms layer-by-layer upon a substrate in vacuum. Heated effusion cells produce beams of atoms or molecules which condense on the substrate to form the desired compound. The use of MBE for $Hg_{l-x}Cd_xTe$ is complicated, in comparison with III-V compounds, by the rapid and incongruent decomposition of material that occurs at elevated temperatures and by the high vapor pressure of Hg necessary to maintain stoichiometric integrity. The very high vapor pressure of Hg limits the substrate temperatures. Above 200 °C, the vapor pressure of Hg is too high to be compatible with MBE, while below about 170 °C the mobility of adatoms on the substrate surface is sufficiently reduced that poor crystallinity results. The electrical, optical and structural properties of $Hg_{l-x}Cd_xTe$ epilayers indicate their excellent quality within a certain area (9 - 16 cm²). Disadvantages of MBE include the high cost and low throughput of present research machines [47]. The slow growth rate of $Hg_{l-x}Cd_xTe$ alloy due to low Hg sticking coefficient at low temperature, and the

high consumption of elemental Hg are also major difficulties in MBE growth of $Hg_{l-x}Cd_xTe$ alloys.

2.2.3 OMVPE

OMVPE used in this study is a promising epitaxial growth technique because it has the potential for high throughput as well as reduced substrate temperatures compared to LPE. There are several potential advantages of growing $Hg_{J-x}Cd_xTe$ at a lower temperature using OMVPE growth. Diffusion processes are reduced at lower temperatures, resulting in sharper $Hg_{J-x}Cd_xTe/CdTe$ interfaces. The equilibrium density of Hg vacancies decreases sharply with temperature [50]. These Hg vacancies can participate in substitutional diffusion and are sites for incorporation of impurities. Since the equilibrium vapor pressure of Hg over $Hg_{J-x}Cd_xTe$ depends exponentially on temperature, the use of a lower growth temperature can result in a significant reduction in the consumption of high purity Hg with a concomitant reduction in the generation of toxic mercury waste. Finally the consumption of the expensive and toxic organotellurium compound may be reduced by using a less stable species.

The OMVPE process, applied to $Hg_{1-x}Cd_xTe$ epilayer growth, involves the use of a carrier gas to transport the organometallic compound of Cd and either vaporized elemental Hg or an organometallic Hg compound with a hydride or organometallic compound of Te into a reaction chamber containing a heated substrate. The gas phase reactant mixture is subsequently pyrolyzed under appropriate experimental conditions of temperature, pressure, and relative concentrations of the reactants to produce the II-VI ternary composition desired. The initial mixing of gaseous reactants normally takes place at room temperature; pyrolysis occurs in the vicinity of the substrate at elevated temperatures (up to ~ 600 °C) in a suitable reaction chamber, with the heated substrate (and/or its

support) normally supplying the required thermal energy to promote the pyrolysis reaction. Somewhat different procedures are required when elemental Hg, instead of an organometallic Hg compound, is used as the reactant.

The $Hg_{1-x}Cd_xTe$ composition is controlled primarily by the concentration ratios of the reactants used. For example, if DMCd, MATe, and elemental Hg, are used as reactants, as used in this experiment, the simultaneous formation of CdTe and HgTe by pyrolysis in H₂ produces a $Hg_{1-x}Cd_xTe$ ternary alloy. The composition is determined by the relative concentrations of the three organometallic compounds or the two organometallic compounds with elemental Hg and by the rates of CdTe and HgTe formation.

The OMVPE process for $Hg_{l-x}Cd_xTe$ semiconductor thin film growth has several unique advantages. The most important advantage of this process is its tremendous flexibility. Basically simple apparatus design and operating procedures provides a high degree of process control. Multilayer structures can readily be grown on a given substrate in almost any desired combination of composition, thickness, and electrical properties in a single sequence of operations in the same apparatus by appropriate switching of reactants and dopants into and out of the carrier gas stream. The growth chamber, whether of the vertical or horizontal configuration, is typically made of clear fused quartz and thus provides the crystal grower with almost completely unobstructed visual access to the growth site at the substrate surface, at least in the critical early stages of layer growth. Intentional impurity doping of the grown layers, in the form of appropriate organometallic compounds or hydrides, can be accomplished by the simple expedient of adding suitable doping compounds directly to the carrier gas stream along with the main reactants. The handling of reactants is simple because the reactants employed are either gaseous or liquid at room temperature. Only one high-temperature control region is required over the entire reactor system.

The OMVPE process for $Hg_{l-x}Cd_xTe$ layer growth also has some distinct disadvantages. The most serious one to date has been the difficulty in obtaining organometallic compounds (specifically, metal alkyls) and metal hydrides of adequate purity for use in growth of ultrahigh-purity semiconductors that challenge the best quality produced by other epitaxial growth techniques and fully exploit the inherent potential of the OMVPE process.

There are two OMVPE approaches to $Hg_{l-x}Cd_xTe$ layer growth; (1) photo- or plasma-assisted and (2) unassisted pyrolytic. The first approach uses photo or plasma stimulation to decompose Cd and Te alkyls. The second approach, which is the one we use, is better suited to uniform $Hg_{l-x}Cd_xTe$ layer growth because the $Hg_{l-x}Cd_xTe$ composition is completely controlled by the arrival rate of the growth species at the growth surface. Photo- or plasma-assisted OMVPE, on the other hand, requires simultaneous control of several variables to grow $Hg_{l-x}Cd_xTe$ alloy with uniform composition. In addition, many impurities contained in Cd and Te alkyls (which contain elements that are electrically active in $Hg_{l-x}Cd_xTe$) will not decompose below 500 °C, unless they are photo- or plasma-stimulated to do so. A more detailed comparison of these approaches, photocatalysis and unassisted pyrolysis, is presented in the table 2.2.

To date, two OMVPE growth techniques for $Hg_{1-x}Cd_xTe$ materials, using unassisted pyrolytic approach, have been developed for producing layers with high quality and a uniformity in composition that is suitable for large device arrays. These are the interdiffused multi-layer process (IMP) [51-53] and the more traditional direct alloy growth (DAG) [13, 54-57] process. In the IMP process, alternate layers of HgTe and CdTe are sequentially grown under conditions optimized for the respective binaries, then homogenized by heat treatment at higher temperatures. This enables the growth of uniform layers over large area. The lattice mismatch between HgTe and CdTe, however, is about 0.3% and the

Characteristics	Unassisted Pyrolysis	Photocatalysis
Large area uniformity	Uniform T _s	Uniform T _s Uniform light intensity
Junction abruptness	Low T _s /High R _g	Low T _s /High R _g
Thickness control	Control Q	Control Q, $T_{\rm s}\!\!,$ and $I_{\rm light}$
Alloy composition	Control Q	Control Q, T_s , I_{light} and stimulated gas reactions
Background impurities	Reduced incorporation due to low T _s	Development of higher purity reactants
Chemical doping	Control Q	Control Q, T _s , I _{light} and light stimulated gas reactions.

 Table 2.2 Process parameter comparison between unassisted pyrolysis and photocatalysis.

possibility of defect generation exists at the CdTe-HgTe interface when the individual layer thicknesses are above a critical value of ≈ 1500 Å. Also the nucleation of CdTe on HgTe may be difficult to control for thin layers of ≈ 100 Å. In DAG process, all precursors are simultaneously introduced into the reactor to grow the Hg_{1-x}Cd_xTe alloy of the required composition. The control of layer composition over a large area has been reported to be difficult using this process. However, more advanced device structures with abrupt interfaces and uniform doping with depth are easier to achieve with this process.

The first $Hg_{1-x}Cd_xTe$ epilayers by OMVPE growth technique were made by Irvine *et al.* [13] using a horizontal reactor at atmospheric pressure. Diethyltelluride (DETe), dimethylcadmium (DMCd), and elemental Hg were used as reactants and H₂ gas as carrier gas. The composition of $Hg_{1-x}Cd_xTe$ epilayers was from x=0 to x=0.3 at a substrate temperature of 410 °C. After this report, many researchers have concentrated on the development of a repeatable $Hg_{1-x}Cd_xTe$ ($0 < x \le 0.7$) photodiode and HgTe/CdTe superlattice growth process, which is important for the high-speed far-infrared detectors that can be operated at or near room temperature. A key requirement for a successful growth process is a low substrate temperature (T_s), to reduce defect concentrations and especially to minimize diffusion effects.

In the OMVPE growth of $Hg_{1-x}Cd_xTe$ the lower limit to the growth temperature is governed by the pyrolyzing efficiency of the tellurium source. Typical growth temperature for $Hg_{1-x}Cd_xTe$ grown by OMVPE are between 360 -400 °C using DMCd as the Cd source and diisopropyltelluride (DIPTe) as the tellurium alkyl [58-60]. Stability considerations by Hoke *et al.* [61] have shown that increasing the chain length of the simple alkyls beyond two carbon atoms had very little effect on reducing the decomposition temperature of the alkyl. However, methyl substitution at the α -carbon atom had a dramatic effect on the useful

growth temperatures for HgTe, typically from 410 °C for DETe and dipropyltelluride (DPTe) to 350 - 370 °C for DIPTe and to 280 - 300 °C for ditertiarybutyltelluride (DTBTe). Instability in the organometallic can also be achieved by introducing unsaturation into the organic group attached to the Te atom. Thus Korenstein *et al.* [62] have grown HgTe at 180 °C using diallyltelluride (DATe) and Lichtmann *et al.* [63] have used MATe to grow CdTe at 250 °C reporting growth rates as high as 30 μ m/h at 290 °C. More recently Bhat *et al.* [64] have made a study of the use of MATe growing mainly HgTe although a few preliminary layers of Hg_{1-x}Cd_xTe were grown. They showed smooth surfaces at 320 °C but detailed characterization of epitaxial quality is not yet available. The stability of these precursors has yet to be fully assessed.

3. II-VI OMVPE SYSTEM AND ARIIV REACTOR DESIGN

3.1 OMVPE System

The conventional reactor configuration of $Hg_{J-x}Cd_xTe$ OMVPE system has shown problems, with respect to reproducibly growing clean, low defect density, large area ($\geq 14 \text{ cm}^2$), $Hg_{J-x}Cd_xTe$ alloy epilayers having precisely controlled x($0 \leq x \leq 1$), with compositional uniformities better than $\pm 1\%$ of the second decimal place in x and excellent electrical uniformities ($\pm 1\%$). Since these are necessary for the purpose of this study, we designed and constructed an all stainless steel reactor system and a modified inverted-vertical (IV) chamber (to be discussed in the next section) which can facilitate the delivery of Hg to the growth surface, without sacrificing the uniform growth characteristics obtained in IV reactor; because the thickness, composition and doping uniformities obtained in the IV reactor configuration exceed uniformity requirements for staring arrays [65-67].

The main components of an OMVPE reactor system designed for $Hg_{1-x}Cd_xTe$ epilayer growth are (1) a source of high-purity carrier gas, in most instances (but not necessarily) H_2 ; (2) temperature-controlled reactant source bubblers, usually in the form of specially designed stainless steel or commercially available stainless steel; (3) gas flow manifold and control system, including appropriate multiple gas flow paths with suitable valving and electronic mass-flow controllers, for achieving the desired reactant concentrations at the proper times in the substrate region; (4) a reaction chamber made of high-purity fused quartz for good visibility into the layer growth region, enclosing the substrate-supporting fused quartz pedestal and including a means of heating the substrate directly and controlling its temperature; and (5) a gas exhaust line, with appropriate filters for particle removal and provision for burn-off of H₂ and an evacuation pump for the gas

manifold system. These components are shown schematically in the block diagram in Figure 3.1.

In simplified form, the OMVPE reactor system designed for the growth of epitaxial $Hg_{1-x}Cd_xTe$ layers on suitable substrates is shown in Figure 3.2. Ultra high-purity H₂ from a Nano-chem purifier and semiconductor grade Ar were directed, by suitable valving and parallel gas lines, toward a series of electronic mass-flow controllers (MFC). As shown at the center of Figure 3.2, two of these carrier gas paths, after passing through MFC, join the input line of the gas manifold. This input line is connected to the main reactant bubblers of DMCd and MATe and the bubbler of hydrazine (N_2H_4) . The bubblers were maintained by thermostatted refrigeration baths at the desired temperature. Temperature control of the elemental Hg bubbler and the Hg delivery line, extending from the bubbler to the annular chamber ("reactor" in the Figure 3.2), was achieved by wrapping electrical heater tape around both components. This purpose was to prevent Hg condensation in the Hg delivery line, which was 10 °C warmer than the Hg bubbler, during the growth. The reactor in the Figure 3.2 is an invertedvertical configuration (to be discussed in detail in the next section). The filtered gas exhaust line and burn-off unit at the upper right of particle trap were located at the top of the hood which houses the OMVPE reactor system. The rotary-vane vacuum system was connected to the reactor for system leak checking.

The gas lines of the system were made exclusively of Type 316 stainless steel tubing, which was carefully cleaned and properly pickled and passivated on the interior surface prior to assembly. All permanent joints were formed by heliarc welding, or VCR fittings. Gas-activated solenoid-operated fast-response valves were used exclusively in the manifold system, with a low-dead-space construction to minimize the mass-flow inertia of the gas handling of the system and to



Figure 3.1 Block diagram of OMVPE reactor system for II-VI epitaxial layer growth





Figure 3.2 Simplified schematic diagram of OMVPE reactor system for II-VI epitaxial growth.

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maximize flow-rate control critical to various steps in the OMVPE growth sequence.

3.2 ARIIV Reactor Design

There are several fundamental generic problems that have to be overcome in the development of an ideal reactor configuration. The first of these is the intrinsic difficulty of devising a laminar, mass transfer controlled system having a high degree of spatial uniformity. Boundary layer formation in the *entrance regions* would strongly mitigate against achieving such uniformity.

The second of these problems is that in order to achieve thermal decomposition a heated substrate has to be employed; the presence of such heated surfaces will give rise to thermal natural convection, which again tends to produce spatial non-uniformities in the transport rates at the solid surfaces.

The Spire reactor has provided an ingenious solution to this dilemma by seeking to establish a balance between the forced and the buoyancy driven convective flows - achieving good results, but only a partial success. Furthermore, the production of heterostructures with a good degree of abruptness could be quite difficult in such a system.

Numerous recent efforts have been aimed at the utilization of a stagnation flow concept, where a gas stream, possibly a distributed gas stream is made to impinge on a single wafer. These approaches represent promising avenues, but the extent to which the ultimate process objectives can be achieved (i.e. a very high degree of spatial uniformity, in the 1 - 2% range and a high degree of abruptness) is still not clear.

The third problem of conventional $Hg_{l-x}Cd_xTe$ reactor, whether of the horizontal or the vertical configuration, is the introduction of hot Hg liquid into the upstream portion of the OMVPE reactor that leads to a lack of process control,

because the hot Hg liquid reacts with Te and Cd alkyls. This configuration also should keep the entire reactor warm (about 240 °C), to prevent the elemental Hg condensation in the reactor chamber wall. As result of the hot Hg vapor and the high temperature of hot reactor wall, the pre-crackings of Cd and Te alkyls is produced before the reactant gases reach to the growth surface and causes an unpredictable composition of the Hg_{1-x}Cd_xTe alloy epilayer.

Considerable attention, therefore, was given to the design of the reactor tube, to facilitate elemental Hg transport to the growth surface in such a way that reactions between hot elemental Hg and organometallics in the system plumbing and upstream portions of the reactor chamber is avoided. Uniformities of thickness, composition and dopant incorporation superior to any other thin film growth process have been demonstrated, repeatably, in the IV reactor [65-67].

The IV reactor assembly includes a reactor tube, a reactor cap, a fused quartz pedestal, a graphite susceptor, and a thermocouple sheath. The key difference between the IV reactor [67] and the ARIIV reactor is the addition of an annular reactant inlet (ARI), shown in Figure 3.3. The Hg delivery approach used here is an important modification to the proven IV reactor design, in order to employ it for Hg_{1-x}Cd_xTe growth. The unassisted pyrolytic OMVPE approach for Hg_{1-x}Cd_xTe alloy growth consisted of the utilization of Hg vapor, and Cd and Te alkyls which completely decompose about 100 times faster than their total contact time with a growth rate is independent of substrate temperature, and it is only necessary to control the flow rates of Hg vapor, and the Cd and Te alkyls in order to control the compositions.

Our unique approach delivers Hg to the growth surface, through an annular reactant inlet, shown in Figure 3.3. This approach permits Cd and Te alkyls to be



Figure 3.3 Crosss-sectional drawing of the ARIIV reactor tube assembly.

introduced in the normal way (from the bottom of the reactor), while heated elemental Hg is introduced into the reactor at the substrate surface through the annular reactant inlet (ARI). This eliminates Hg liquid from the upstream gas flow and increases the impingement of Hg vapor onto the growth surface by a factor of more than 100 for a given Hg vapor flow rate; it also increases the rate at which Hg vapor can be delivered to the growth surface. In addition, this approach facilitates complete mixing of Hg with Cd and Te alkyls over the entire substrate surface.

In the ARIIV reactor, transport of Hg vapor to the ARI port is accomplished by introducing it into an annular reactor tube wall jacket near the top of the reactor. The annular jacket inlet was modified from a single inlet, which was designed at the first stage of system construction, to a double inlet at diametrically opposite locations, in order to provide uniform pressure around the annular jacket. The annular jacket is enclosed by electrical band heater tape to prevent Hg condensation from the jacket wall. The purpose of the vertical offset between the entry port into the jacket and the ARI port is to obtain uniform dispersal of Hg in the jacket before it enters the reactor at the ARI port. The Hg enters the reactor just below the substrate from a 360° arc which lies in a plane perpendicular to the reactor axis. The distance between the outside wall of the reactor tube and the inside wall of the jacket is 2 mm. This cross section is reduced to 1 mm at the ARI port to increase the Hg velocity as it enters the reactor, and to prevent other gases from back-streaming into the ARI port. The ARI port protrudes approximately 1 mm into the reactor and injects Hg into the reactor in a plane perpendicular to the reactor axis.

In the ARIIV reactor arrangement, the inside diameter (ID) of reactor increases from 4 mm at the primary inlet port to a maximum of 89 mm at the top of the funnel shaped portion of the reactor. The angle between the reactor axis

and the reactor tube walls in the funnel shaped portion of the reactor is 11.5°. Above this portion, the reactor the walls are parallel to the reactor axis. An 83 mm ID pedestal support ring is fused to the inner wall of the reactor tube, 70 mm below the mating surface of the reactor tube mating flange. A 45 mm diameter by 105 mm long glass tube is welded to the inside of the reactor, to form a reservoir condensed Hg.

The reactor cap is fitted to the top of the reactor tube by means of mating flanges which are compressionally loaded against a viton gasket to form a vacuum tight seal. A 21 mm ID reactor exhaust port and a 6.3 mm ID thermocouple sheath inlet port are located at the top of the reactor cap. The thermocouple sheath inlet port opening is centered with respect to the reactor tube and it is parallel to the reactor axis. The exhaust port is connected to the system exhaust by a 24 mm ID stainless steel Ultra-Torr fitting. The reactor cap system consists of 316 stainless steel.

The pedestal is a cup-shaped fused quartz cylinder, as shown in Figure 3.4. The pedestal is coaxially supported within the reactor tube by an 86 mm diameter support collar which rests on the top surface of the pedestal support ring. The length and diameter of the pedestal are 59 mm and 74 mm, respectively. The side wall and the bottom surface are 2 mm thick.

The openings in the side wall, at the top of the pedestal, provide a flow path to the reactor exhaust for product gases. This is only one of several possible methods which can be employed to provide a flow path to the reactor exhaust.

The bottom surface (base) of the pedestal has a 46 mm centered circular opening, as indicated in Figure 3.5. The inside surface of the pedestal base has a 0.1 mm deep recess which extends, concentrically, 2.5 mm beyond the 46 mm diameter opening. Substrates (50 to 51 mm diameter) are supported on the 2.5 mm annulus, growth face downward, on the inside bottom of the pedestal. Smaller



Figure 3.4 Cross-sectional view of fused quartz pedestal.

circular or irregularly shaped substrates are supported on a 51 mm diameter, 1 mm thick molybdenum mask which rests on the 2.5 mm annulus of the pedestal base. In this situation, substrate growth surfaces are exposed to the reactant gases through one or more openings in the mask, as shown Figure 3.5.

Heating is accomplished by a direct resistance pancake heater, which is made of flexible high watt-density electric sheath heater wire coiled in the stainless steel heater retainer. The heater retainer rests on a graphite susceptor which in turn sits directly on the back surface of the substrate and inside the pedestal. The graphite susceptor is 63.5 mm and 57.5 mm in bottom and top diameter, respectively, and 5 mm thick. The bottom of the graphite susceptor also contains a 51 mm diameter concentric recess. A susceptor with a 0.1 mm deep recess is used for mounting 2 inch diameter substrates. The pedestal and susceptor recesses, in conjunction with the substrate (or substrate and mask), form an interlocking arrangement which centers the substrate and susceptor with respect to the reactor tube axis.

In the absence of exhaust pumping, the total flow rate that minimizes back streaming near the reactor tube walls (optimum total flow rate) is determined by the exhaust pressure and by the smallest cross-sectional area through which reactant products must pass, downstream from (above) the primary reactor inlet port. The reactor exhaust pressure for all experiments reported in this paper was 760 Torr. The minimum cross-sectional area for gas flow through the reactor was 3.46 cm², located at the exhaust port. The optimum total flow rate through the reactor is 2.9 standard liters per minute (SLPM). This flow rate minimizes the size of eddy currents, and localizes their position to the reactor tube wall just below the bottom surface of the pedestal.

Another unique aspect of this OMVPE $Hg_{l-x}Cd_xTe$ epilayer growth study, aside from the ARIIV reactor tube, is the hydrazine source, the use of which



Figure 3.5 Enlarged view of the fused quartz pedestal, substrate, mask, and heater assembly.

results in a profound improvement in $Hg_{l-x}Cd_xTe$ growth quality, as demonstrated by experiments performed by Dr. Parsons and his group at Hughes Research Labs. This great improvement in results was achieved when a small amount of hydrazine was added to the gas flow to increase the reactivity of Hg at the substrate surface during Hg_{1-x}Cd_xTe growth. The initial motivation for using hydrazine was to prepare LPE grown $Hg_{l-x}Cd_xTe$ surfaces for surface passivation coatings. The requirements of this process were that the $Hg_{l-x}Cd_xTe$ surface, after hydrazine exposure, be clean and that the $Hg_{l-x}Cd_xTe$ surface concentration must match that of the bulk $Hg_{1-x}Cd_xTe$. Hydrazine was chosen because it is an excellent reducing agent, and the atomic hydrogen from the hydrazine molecule reacts with and removes free Te from the $Hg_{l-x}Cd_xTe$ surface. Results of Electron Spectroscopy for Chemical Analysis (ESCA) measurements showed that hydrazine did an excellent job of cleaning $Hg_{l-x}Cd_xTe$ surfaces and that it also prevented Hg surface depletion at substrate temperatures up to 300 °C. These results attest that the presence of hydrazine during $Hg_{l-x}Cd_xTe$ growth: (1) increases the Hg concentration in $Hg_{l-x}Cd_xTe$ by a factor of 10 for a given Hg vapor concentration at the growth surface, (2) yields $Hg_{l-x}Cd_xTe$ epilayers with uniform Hg concentrations throughout the epilayer thickness, (3) reduces the carbon and oxygen concentrations in $Hg_{l-x}Cd_xTe$ to $< 10^{19}/cc$ and reduces alkali metal concentrations to $< 10^{15}/cc$, and (4) prevents depletion of Hg near Hg_{1-x}Cd_xTe epilayer surfaces.

3.3 Inverted-Vertical Reactor Design Purpose and Function

The inverted-vertical configuration, wherein reactant gases are introduced below a downward facing substrate and the reactant products are exhausted above the substrate, utilizes the buoyancy effect to remove hot reactant products from the reactor and to suppress the formation of backstreaming eddy currents [67]. The purpose of the 11.5° angle between the tube wall and the reactor axis is to minimize the dead volume between the inlet port and the substrate. The 11.5° angle was selected because it is the smallest angle that can be reproducibly made by conventional glass shop techniques.

Stagnation point flow and symmetry are the goals of most vertical reactor designs. The IV reactor achieves these goals by virtue of the inverted configuration and the orientation of the substrate surface perpendicular to the gas flow direction. Back-streaming eddy currents do not form at the growth surface because the buoyancy of the hot gases keeps them in contact with a downward facing surface until they are pushed beyond the radius of that surface and permitted to flow in an upward direction.

The reactor exhaust port, in the cap, is not centered with respect to the reactor axis. This asymmetry in the IV reactor design does not effect the growth uniformity when the reactor is used at atmospheric pressure because the pressure gradient in the reactor is created at the inlet port. However, growth uniformity under low pressure operating conditions, where the pressure gradient in the reactor is created at the exhaust port, may require that the exhaust port be concentrically positioned above the pedestal. Concentric positioning of the exhaust port would provide the best possible uniformity in pumping speed about the pedestal radius.

The arrangement of the pedestal, substrate, and susceptor within the reactor performs two functions, aside from the primary function of attaining the IV configuration, which are describe below.

Particulates cannot fall onto the growth surface because the substrate faces downward. Flaking of reactor tube wall deposits rarely occurs, except during very long growth runs, because this reactor tube is designed for cold wall pyrolytic OMVPE growth. When flakes do fall from the reactor tube wall, they remain just above the primary inlet port; where they swirl about in the inlet gas stream. The flakes do not enter the 4 mm diameter inlet tube until it is disconnected from the 6.3 mm OD stainless steel inlet tube.

Shielding the growth surface from impurities emanating from hot reactor parts is the other function of the pedestal design. The reactant and product gases do not come into contact with any hot surface, except the substrate, until they are downstream from (above) the growth surface. The hot reactor components are located in a region of high velocity (pedestal side walls), or a region of minimal flow (inside the pedestal) which is well isolated from the input gas stream and the growth surface.

4. GROWTH AND CHARACTERIZATION PROCEDURES

4.1 Substrate and Epilayers Growth Preparation

Growth experiments of CdTe, HgTe, and Hg_{1-x}Cd_xTe epilayer crystals were performed on 51 mm diameter InSb and 25.4 to 51 mm diameter CdTe substrates. InSb was selected for CdTe epilayer growth study because it has an excellent lattice match (\cong 0.05 %) and its thermal expansion coefficient matches that of CdTe. Moreover, it is available in the form of large diameter substrates with good crystal quality. The bulk InSb has fewer defects than CdTe because it is grown at a much lower temperature (\approx 530 °C) than bulk CdTe (\approx 1090 °C). The full width at half-maximum (FWHM) of x-ray diffraction curves for InSb is typically 10 arcsec for as-grown materials. The theoretical FWHM of x-ray diffraction curves for CdTe is about 11 arc-sec whereas typical values obtained in high quality, bulk CdTe often exceed 34 arc-sec [68].

High resistivity (100) CdTe wafers, 5° misoriented toward (110), were used as substrate material for epitaxial growth of HgTe and Hg_{1-x}Cd_xTe layers, in order to study the surface morphologies, the growth rates of HgTe and Hg_{1-x}Cd_xTe layers, and the electrical studies. InSb (100) substrates were used for CdTe and HgTe growth rates studies. Substrates used for growth runs were degreased in warm trichloroethane (TCE), acetone, and methanol followed by a deionized water rinse and a nitrogen blow dry, before they were chemically etched. Most large area CdTe substrates (larger than 8 cm²) were mechanically repolished for reuse after growth and analysis of Hg_{1-x}Cd_xTe epilayers, because of the limited availability of CdTe substrates. This procedure was performed by II-VI Crystal Corporation. These reused substrates were degreased in warm TCE, acetone, and methanol for 1 min. each and then chemically etched, to remove the scratched substrate surface layer, in 2% bromine in methanol solution at room temperature for 20 seconds, followed by a final rinse in deionized water and a dry nitrogen blow, before loading into the reactor. The InSb surfaces were chemically etched in mixture of 63 HF, 36 HAc, and 1.4 HNO₃ solution at room temperature for 14 seconds, followed by a final rinse in deionized water and a dry nitrogen blow.

After loading the substrates, the reactor chamber was first evacuated with a rotary-vane pump, then pressurized to 5 psi with high purity Argon gas (Air Products, research grade) and maintained for 5 min., to check the system for leaks. This procedure was repeated twice and then the reactor chamber was brought to growth temperature under the flow of hydrogen carrier gas (Airco, VLSI grade), which was passed through Nano-Chem 3000 purifier system (Semi-Gas system). The hydrazine, flow rates (Q_{NH}) between 0.0012 and 0.012 standard cubic centimeter per minute (sccm), was also introduced at the same time through the primary inlet port of the ARIIV reactor with H₂ carrier gas at the beginning stage of growth runs, in order to etch the substrate internally during the phase of heating the substrate up to growth temperature. When CdTe epilayer growth runs were conducted, 100 sccm H₂ carrier gas was introduced through the Hg delivery inlet to prevent back-streaming into the Hg plumbing.

4.2 Cadmium Telluride Growth

The purpose of these experiments was to determine CdTe growth rate as a function of substrate temperature (T_s) and MATe alkyl flow rate, and to calibrate the effect of substrate and ratio of the flow rate between DMCd and MATe on surface morphology and crystallinity.

The flow rates of DMCd (Q_{Cd}) and MATe (Q_{Te}) were stabilized through a vent-run manifold before initiating growth, by simultaneously introducing DMCd and MATe alkyls into the reactor.

One parameter examined in the first CdTe epilayer deposition experiments using the ARIIV reactor was the substrate temperature dependence of the growth rate. The growth rate as a function of T_s was determined at a constant flow rate ratio of DMCd to MATe between 290 °C $\leq T_s \leq 360$ °C. The flow rates of DMCd and MATe precursors were fixed at 0.768 sccm (bubbler maintained at 15 °C) and 0.78 sccm (bubbler maintained at 18 °C), respectively, in these experiments.

The growth rate of CdTe was correlated with (T_s) , the flow rate of MATe, and the flow rate of hydrogen through the ARI. The purpose of these experiments was to determine the effect of the IV reactor design modifications, and the heating and temperature measurement modifications on results obtained in the IV reactor [63]. Therefore, the growth rate of the CdTe epilayer which was deposited on one quarter of a 2 inch (100) InSb substrate was first evaluated as a function of T_s by measuring the epilayer thickness on cleaved cross-sections. The CdTe epilayers on (100) CdTe substrates offset 5° towards (110) were also grown for comparison purposes.

The DMCd to MATe flow rate ratio (Q_{Cd}/Q_{Te}) which yielded the best asgrown surface morphology was determined at T_s =300 °C. The selection of T_s =300 °C was based on HgTe growth results, as discussed below. The pressure during the growth was one atmosphere and the total gas flow through the reactor tube was 2.9 SLPM. The hydrazine, which is a unique reducing agent source in this OMVPE CdTe epilayer growth, was also introduced at the same time through the primary inlet of the reactor tube at a hydrazine flow rate of of 0.011 sccm (bubbler maintained at 15 °C).

4.3 Mercury Telluride Growth

The HgTe experiments were performed to determine the growth rate of HgTe epilayer as a function of the flow rate of MATe alkyl, the maximum of Hg to Te

ratio that would yield stochiometric HgTe (free of Te precipitation), and the growth temperature of $Hg_{1-x}Cd_xTe$ epilayer based on Hg reactivity and vapor pressure properties and pedestal position.

The flow rate of Hg was controlled by the Hg bubbler temperature and the flow rate of H₂ carrier gas through it. Hg vapor was tranported from the bubbler to the inlet port of the annular jacket leading to the ARI port, through 6.4 mm pluming. The temperture of the pluming and the annular jacket were kept at least 10 °C higher than the Hg bubbler temperature ($290 \le T \le 310$), to prevent Hg condensation on the plumbing and jacket walls. The maximum temperature of the reactor tube wall below the substrate - which was heated by thermal conduction from the annular jacket wall - was less than 90 °C. The hydrazine flow rate of 0.012 (bubbler temperature was increased to 18 °C) was introduced through the primary inlet of the reactor tube in the HgTe epilayer growth experiments. The pressure during the growth run was one atmosphere and the total gas flow through the reactor tube was maintained at 2.9 SLPM.

The ratio of Hg to MATe flow rate which yielded HgTe layers which were not Te rich were determined at $T_s=300$ °C. The optimum position of substrate which maximize the growth rate of HgTe epilayer was 6.3 mm above the plane of the ARI.

4.4 Mercury Cadmium Telluride Growth

The growth rate and composition of $Hg_{I-x}Cd_xTe$ epilayers were determined as a function of Cd flow rate at constant Hg flow rate, MATe flow rate, and T_s . The initial growth runs of $Hg_{I-x}Cd_xTe$, therefore, were made with a variation of the Cd flow rate, 0.0755 sccm $\leq Q_{Cd} \leq 0.215$ sccm (bubbler maintained at 10 °C), at the following fixed parameters: the Hg flow rate of 226 sccm (bubbler maintained at 315 °C), flow rate of MATe of 1.55 sccm (bubbler maintained at 18 °C), and $T_s=300$ °C.

The reason that above parameters were was that these constant parameter gave the highest growth rate of HgTe and the Cd flow rate would control the composition of $Hg_{1-x}Cd_xTe$ epilayer. After several growth runs of $Hg_{1-x}Cd_xTe$, it was found that the small amount of DMCd flux in the gas phase determined the composition of $Hg_{1-x}Cd_xTe$ epilayer and the surface morphology.

The growth rate of the Hg_{1-x}Cd_xTe layer as a function of T_s and a function of MATe flow rate was also determined over a wide range of growth conditions, in order to establish the optimum growth conditions for Hg_{1-x}Cd_xTe epilayer, based on each growth run result. These growth experiments also gave two important interactions; (1) the substrate temperature determined the composition of asgrown Hg_{1-x}Cd_xTe epilayers due to the reactions between DMCd alkyl and the increasing cracking rate of MATe alkyl with increasing temperature, and (2) the growth rate of Hg_{1-x}Cd_xTe epilayers at higher T_s (300 °C < T_s ≤ 330 °C) increased as the MATe flow rate increased, but was lower at T_s=300 °C due to the lower elemental Hg sticking coefficient with increasing temperature.

The optimum relationship between the relative flow rate and Hg concentrations ($0.2 \le x \le 0.3$) of Hg_{1-x}Cd_xTe epilayers was determined, based on the above results, and then the maximum growth rate conditions of Hg_{1-x}Cd_xTe epilayer were fixed. The reason for focusing on a particular Hg concentration was that this composition range of Hg_{1-x}Cd_xTe crystals is used for IR detectors and that this would minimize the number of experiments required.

The optimum conditions which could produce the maximum growth rate of $Hg_{1-x}Cd_xTe$ with $x \equiv 0.2$ were determined as a function of DMCd flow rate; where the flow rates of Hg amd MATe were 174 sccm and 1.55 sccm, respectively, and T_s was 300 °C, and the pedestal was positioned such that the

substrate surface was 6.3 mm above the plane of the ARI. The flow rate of hydrazine (maintained at 18 °C) was 0.012 sccm. The pressure during the growth run was one atmosphere and the total gas flow through the reactor tube was again maintained at 2.9 SLPM, as in the case of CdTe and HgTe growth.

4.5 Characterization Procedures

The single crystallinity of CdTe, HgTe, and Hg_{*l-x*}Cd_{*x*}Te epilayers grown on InSb or CdTe substrates was confirmed by employing electron contrast channeling patterns (ECCP). The shape or the symmetry of the bright band in ECCP reveals the orientation of the crystal, the width of the bright band reveals the lattice constant (from which the composition of the crystal can be calculated), and the sharpness of the bright band indicates the crystalline quality of the material. The working distance (the distance from the point source to the sample) employed in the ECCP study was 20 mm and the magnification was 15x in the back scattering electron mode.

Thin epilayers of $Hg_{1-x}Cd_xTe$ (approximately 1000 Å thick to facilitate ellipsometric measurement, because of the absorption of 6328 Å light in $Hg_{1-x}Cd_xTe$) grown on 20 cm² CdTe were analyzed by multiple angle of incidence (MAI) ellipsometry [67, 69, 70], using a 0.6328 µm He-Ne laser light source. MAI ellipsometry technique was employed to analyze the thin epilayer (≤ 1500 Å) composition and compositional uniformity, epilayer thickness and the thickness uniformity, and substrate/epilayer interface abruptness.

The MAI ellipsometry technique is a far more powerful tool for analysis of optically transparent thin films than the standard single angle of incidence ellipsometric technique. A computer program has been developed which permits simultaneous determination of thickness, refractive index (real and imaginary) and birefringence in 3 layers, which are initially assumed to be present. In this case the 3 layers which will be anticipated, and analyzed if they exist, are (1) a surface contamination layer, (2) the as-grown $Hg_{l-x}Cd_xTe$ layer, and (3) an interface layer between the $Hg_{l-x}Cd_xTe$ layer and the substrate.

The composition of as-grown $Hg_{l-x}Cd_xTe$ layers ($\geq 3 \mu m$) was determined by a dual beam Fourier transform infrared (FTIR) spectrophotometer. A CdTe substrate was placed in the reference beam to compensate for the transmission from the substrate. The sharpness of the absorption edge of the FTIR spectrum indicates that the layer is reasonably uniform both across the layer and with depth. The periodicity of the interference pattern is directly related to layer thickness, which is given by Equation 2.2 in Chapter 2. The composition of the as-grown $Hg_{1-x}Cd_xTe$ layer, which is related to the band gap of the $Hg_{l-x}Cd_xTe$ crystal, was estimated from the measured thickness and the FTIR transmission curve by taking the energy at which α =500 cm⁻¹. The revised empirical relation between the composition and the band gap in the composition range of $x \leq 0.6$ and x=1, which is the Equation 2.1[17] in Chapter 2, was employed for the as-grown $Hg_{l-x}Cd_xTe$ layers.

The morphology of the $Hg_{1-x}Cd_xTe$ epilayers was examined using a Nomarski contrast microscope and scanning electron microscopy (SEM). Epilayer thickness measurements were also performed by these techniques, which are more accurate than FTIR measurements. Growth rates of CdTe/InSb, HgTe/CdTe or InSb, and $Hg_{1-x}Cd_xTe/CdTe$ were determined by measuring the epilayer thickness after the sample was cleaved and the cross-section etched using defect etchant (to be discussed below). The crystalline properties of as grown epilayers were evaluated using double crystal x-ray diffraction (DCXRD).

The cystal defects of as-grown $Hg_{1-x}Cd_xTe$ epilayer were examined using defect etchant. The most common etchants used for the determination of the dislocation density is HF + H_2O_2 + H_2O [71, 72] for CdTe and HNO₃ + HCl +

 $H_2O + Br_2$ in CH_3OH or $+ Br_2$ in CH_3COOH for $Hg_{1-x}Cd_xTe$ [73]. These defect etchants are known to develop etch pits on only one of the crystallographically opposite (111) surfaces. The Polisar etchant [73] produces etch pits on any plane of $Hg_{1-x}Cd_xTe$ crystals. A mixture of HCl, HF, and chromic acid was proposed for (110) plane of $Hg_{1-x}Cd_xTe$ [74]. These reported etchants were unsuitable for $Hg_{1-x}Cd_xTe$ epilayers grown on (100) CdTe with 5° offset towards (110) plane, in contrast to those reported by Hähnert *et al* [75]. Therefore, Hähnert etchants were used for the studies of distribution of crystallographic defects.

The etchant for $Hg_{1-x}Cd_xTe$ epilayer has a basic composition consisting of 1 HF, 1 CrO₃ (50 wt% CrO₃ in H₂O), and 1 HCl. This mixture was diluted with deionized water. It was found that a dilution of 10 times produced etch pits on the $Hg_{1-x}Cd_xTe$ epilayers (0.0 $\leq x \leq$ 0.4). For the etching procedure, the samples were dipped and stirred in the etching solution for 5 min.

Electrical properties for as-grown $Hg_{1-x}Cd_xTe$ epilayer were characterized by Hall effect measurements using the van der Pauw technique at temperature variation of 50 - 300 K. Hall effect measurements were performed on 5 x 5 mm² sections cleaved from as-grown $Hg_{1-x}Cd_xTe$ epilayers, in order to determine carrier type, carrier concentration, and carrier mobility. Indium point (dot) contacts, 0.3 - 0.5 mm in diameter, were used for the four corners of thin layers using a soldering iron with a small tip. A magnetic field from 1 to 3 K gauss was applied during the Hall effect measurements. The measurements and the calculations were performed by a computer.

5. EXPERIMENTAL RESULTS AND DISCUSSION

5.1 CdTe Epilayer Growth

The cleaved cross-sections used to measure the growth rate of CdTe using a Nomarski contrast microscope is shown in Figure 5.1. The growth rate, plotted in Figure 5.2, increased from 0.5 μ m/h at 290 °C to 3 μ m/h at 360 °C. The increase in CdTe growth rate with increasing substrate temperature is due to the T_s dependent decomposition rate of MATe alkyl.

Substrate temperature independent CdTe growth rates were obtained at or above 360 °C. The 60 °C difference in temperature at which T_s independent CdTe growth rate was obtained in this work, relative to that reported for the IV reactor in reference [65], is attributed to inaccurate T_s measurement in reference [65]. The accuracy of T_s measurements reported here were determined to be within ±1 °C of the actual T_s in the following manner. An assembly consisting of a thermocouple (type K: Chromel-Alumel, 0.0762 mm diameter) junction, placed between two 51 mm diameter Si wafers, was mounted in the ARIIV reactor. The substrate temperature was measured in a H₂ flow rate of 2.9 SLPM through primary inlet port.

The growth rate of CdTe epilayers on InSb substrates increased sublinearly from 3 μ m/h to 11 μ m/h as the flow rates of hydrogen carrier gas through the bubblers of DMCd and MATe were quadrupled at 360 °C. This result is plotted in Figure 5.3.

The flow rate of diluent H₂ through the ARI port ($Q_{H2} \le 1100$ sccm) had no measurable affect on the growth rate of CdTe epilayer. This was confirmed by performing CdTe growth experiments, at identical flow rate and T_s conditions, in the ARIIV and in the IV reactor configurations.





(b)

Figure 5.1 Cleaved cross-sections used to measure the thickness of CdTe/InSb using Nomarski contrast; (a) $T_s=290$ °C and (b) $T_s=300$ °C.



Figure 5.2 Growth rate of CdTe epilayer as a function of substrate temperature.



Figure 5.3 Growth rate of CdTe epilayer as a function of the flow rate of MATe .

The crystal quality of CdTe epilayers grown on InSb substrates was evaluated using the DCXRD measurement technique. An example of the DCXRD of 3 μ m thick CdTe film grown on a (100) InSb substrate at 360 °C is shown in Figure 5.4. The CdTe epilayer had the same orientation as the (100) InSb substrate. The peak position of the rocking curve of the CdTe epilayer, with respect to the InSb substrate peak, gives an estimate of the lattice mismatch between two materials. In Figure 5.4, multiple peaks due to the presence of low-angle grain boundaries are absent, and the FWHM of the CdTe epilayer rocking curve peak was 92 arc-sec.

This broad angle, compared to the high quality of CdTe/InSb epilayers grown by MOCVD [76] and MBE [77], supports the assumption that the FWHM intensity of the peak can be broadened by misorientation and by the lattice strain of the film in the layer [78]. In addition, it should be noted that the FWHM of the InSb substrates prior to deposition were typically in the 30 to 60 arc-sec range, which are not high quality InSb substrates, and the growth of the films almost doubled this value.

CdTe epitaxial layers at T_s = 360 °C were also grown on (100) \rightarrow 5° (110) CdTe for comparison purposes. It was found that the layers grown on CdTe substrates consistently showed superior morphology to those grown on InSb substrates.

The ECCP photo of the CdTe layer, which confirms the single crystallinity of the layer, is presented in Figure 5.5. About 1 cm wide one-fold bright bands can be seen on the ECCP picture, but the bright bands are not clear enough to be used for calculating composition or comparing crystalline quality of the CdTe epilayer. The reason for the vague ECCP is believed to be due to the limitation of SEM equipment, namely, the electron source of scanning deviates too strongly from the ideal point source.






Figure 5.5 Electron channeling contrast pattern of CdTe epilayer.

The crystal orientation of CdTe epilayers grown on (100) CdTe substrates offset 5° towards (110) at T_s = 360 °C was also studied as a function of ratio of DMCd to MATe flow rate (0.5 $\leq Q_{Cd}/Q_{Te} \leq 1$). Under these conditions, all asgrown CdTe epilayers with different Cd to Te alkyl flow rate showed the same orientation as the substrates, which were analyzed using x-ray diffraction pattern measurements prior to being loaded into the reactor chamber. An example of DCXRD of an as-grown mirror-like CdTe epilayer with 3 µm thickness is shown in Figure 5.6. The CdTe substrates used for the II-VI epilayer growths showed two main peaks, which were caused by subgrain boundaries of substrate, as shown in Figure 5.6 (b), which is not acceptable in substrates for high quality epilayers. These poor quality CdTe substrates resulted in large FWHM, as shown in (a) of Figure 5.6.

The evaluation of the surface smoothness (morphology grade) of CdTe epilayers grown at T_s = 360 °C as a function of the ratio of DMCd to MATe flow rate is shown in Figure 5.7. The grades (1 is the best, shinny and mirror-like surface, and 3 the worst, milky, brownish and black surface) shown in Figure 5.7 were determined by Nomarski contrast microscope. Most CdTe epilayers had smooth, mirror-like surfaces, but the CdTe layer with a ratio of 0.75 showed the best surface morphology. A light milky surface morphology was obtained at Q_{Cd}/Q_{Te} greater than 0.75. Brownish, faceted surfaces were obtained at Q_{Cd}/Q_{Te} less than 0.5. A difference in the growth rate of CdTe epilayer was not observed at $0.5 \le Q_{Cd}/Q_{Te} \le 1$.

Some CdTe layer surfaces contained microterraces, hillocks, and pits, which are characteristic of the (100) orientation and appear to be associated primarily with improper surface cleaning, or physical defects in the substrate surface [79]. The microterraces in this study, however, were usually observed at Q_{Cd}/Q_{Te} lower







Figure 5.7 Surface morphology grade as a function of ratio of DMCd to MATe flow rate.

than 0.6 and the pits at Q_{Cd}/Q_{Te} greater than 0.8, as shown in Figure 5.8. Asgrown CdTe layers with physical defects showed hazy surface structure at the initial stage, but this feature diminished as growth proceeded and then the layers became very smooth after about 2 μ m of growth.

5.2 HgTe Epilayer Growth

The growth rate of HgTe was determined as a function of T_s in the temperature range between 290 - 330 °C. The flow rates of elemental Hg and MATe were fixed at 226.0 sccm (bubbler maintained at 315 °C) and 1.55 sccm (bubbler maintained at 18 °C), respectively. These growth conditions yielded HgTe epilayers with mirror-like sufaces.

The growth rates of HgTe epilayers grown on (100) $5^{\circ} \rightarrow$ (110) CdTe substrate ranged from 0.75 µm/h to 2.7 µm/h, as plotted in Figure 5.9. The growth rate of HgTe epilayers decreased at T_s greater than or less than 300 °C. The growth rate as a function of T_s suggests that below 300 °C, the MATe cracking rate decreases more rapidly than does the Hg volatility; and that above 300 °C, the MATe cracking rate increases less rapidly than does the Hg volatility.

When the flow rate of MATe alkyl was doubled from $Q_{Te}=1.55$ sccm to $Q_{Te}=3.1$ sccm at $T_s=300$ °C, the growth rate of HgTe did not change. This result suggested that more Hg was needed to increase the growth rate of HgTe. The growth conditions which gave the highest growth rate of HgTe, $Q_{Hg}=226.0$ sccm and $Q_{Te}=1.5$ sccm was used for the first Hg_{1-x}Cd_xTe epilayer growth experiments.

The distance above the plane of the ARI inlet, at which the substrate surface was located, significantly affected to the growth rate of HgTe (about 0.3 μ m/h increment) at above conditions. The separation distance which gave the highest growth rate of HgTe, 6.3 mm, was used to for all HgTe and Hg_{1-x}Cd_xTe growth experiments.



Figure 5.8 Nomarski contrast micrograph of CdTe epilayers showing microterraces (a) and pits (b).

(b)



Figure 5.9 Growth rate of HgTe epilayer as a function of substrate temperatue.

The as-grown HgTe epilayers grown at 290 °C $\leq T_s \leq 330$ °C were single crystalline. The ECCP showed one-fold bright bands about 1 mm wide, which were single crystalline and indicate a (100) orientation of the grown layer, but as before in the case of CdTe layers the bright bands were not clear enough to be used for comparing crystalline quality of the HgTe layers.

In morphology studies, most HgTe layers grown on CdTe substrates showed smooth and specular surface. Pitted HgTe layers grown at $T_s=330$ °C, shown in Figure 5.10, were observed when the MATe flow rate of 3.1 sccm was used. When the MATe flow rate of 1.55 sccm was introduced at $T_s=330$ °C, the as-grown HgTe surface showed hillocks with the hillock density in the range of 3000 - 5000 cm⁻², as shown in Figure 5.11. However, the HgTe epilayer surfaces grown below 320 °C did not show pits or hillocks. These results suggest that the lower temperature growth of HgTe gives the better surface morphology and the pits and the hillocks surface are probably related to the Te-rich and Hg-rich surfaces, respectively.

5.3 Hg_{1-x}Cd_xTe Epilayer Growth

5.3.1 Growth Rate of Hg_{1-x}Cd_xTe Layers

A typical FTIR transmission curve and a representative Nomarski contrast micrograph of a cleaved cross section showing the $Hg_{I-x}Cd_xTe$ layer, which were used to determine the growth rate and the composition of $Hg_{I-x}Cd_xTe$ epilayers, are presented in Figure 5.12 and 5.13, respectively. The micrographs show the interface, sufficiently clearly to measure the thickness of the $Hg_{I-x}Cd_xTe$ epilayer without SEM, when the defect etchant was used [75].

The growth rate and the composition of $Hg_{l-x}Cd_xTe$ epilayers at $T_s=300$ °C was a function of DMCd flow rate, as shown in Figure 5.14. The $Hg_{l-x}Cd_xTe$



Figure 5.10 Nomarski contrast micrograph of HgTe epilayer showing pits (a) 910x and (b) 448 x.



(a)



(b)

Figure 5.11 Nomarski contrast micrograph of HgTe epilayer showing hillocks; (a) 177 x and (b) 910 x.



Figure 5.12 Typical Fourier Transform of Infrared transmission curve of $Hg_{l-x}Cd_xTe$ epilayer grown on CdTe substrate.





Figure 5.14 Growth rate and composition of $Hg_{J-x}Cd_xTe$ as a function of the flow rate of DMCd.

epilayer growth rate decreased from 4 μ m/h at the DMCd flow rate of 0.0075 sccm to 0.75 μ m/h at the DMCd flow rate of 0.06 sccm. The composition of Hg_{1-x}Cd_xTe epilayers increased from x=0.126 to x=0.359 as the flow rate of DMCd increased from 0.0075 sccm to 0.06 sccm. The increase of Hg_{1-x}Cd_xTe composition, coupled with a decreasing the growth rate of Hg_{1-x}Cd_xTe at higher flow rate of DMCd, indicates that Cd is interfering with Hg nucleation because of the disparity in bond strength between CdTe and HgTe [13].

The presence of the relatively strongly bonding Cd ($\equiv 23 \text{ kcal/mol}$) in Hg₁₋ _xCd_xTe on a growth surface would increase the probability of nucleation of Cd, whereas the more weakly bonding Hg ($\equiv 13 \text{ kcal/mol}$) would be less likely to nucleate on the growth surface. In the absence of Cd, once a nucleating site is formed, further nucleation of Hg in the vicinity of this site is unlikely due to the weak bond strength between Hg and Te. This weak bonding of Hg would enable any adsorbed Hg atoms to diffuse readily across the crystal surface until they reached a growth step, or even to desorb. The presence of strongly bonding Cd will result in the preferential formation of Cd nucleating sites and result in a higher composition alloy and a lower growth rate in the Hg_{1-x}Cd_xTe crystal. Therefore, even a small flux of DMCd determines the composition of Hg_{1-x}Cd_xTe epilayer crystal.

Another $Hg_{l-x}Cd_xTe$ growth experiments at $T_s=330$ °C was performed as a function of MATe flow rate at fixed the DMCd flow rate of 0.045 sccm, in order to check the contribution of DMCd alkyls to the growth rate and the composition of as-grown $Hg_{l-x}Cd_xTe$ epilayer. This result is plotted in Figure 5.15.

The $Hg_{1-x}Cd_xTe$ growth rate at $T_s=330$ °C ranged between 0.25 - 0.7 µm/h as the MATe flow rates increased from 0.776 sccm to 3.102 sccm. The growth rate was doubled when the flow rate of MATe was doubled from 0.776 sccm to 1.55 sccm, but the growth rate was not doubled when the flow rate of MATe was



Figure 5.15 Growth rate of $Hg_{I-x}Cd_xTe$ as a function of the flow rate of MATe.

doubled again from 1.55 sccm to 3.102 sccm. This may be due to the insufficient Hg available for higher $Hg_{I-x}Cd_xTe$ growth rates. The composition of these $Hg_{I-x}Cd_xTe$ epitaxial layers was $x = 0.356 \pm 0.01$ for three different films within the measured MATe flow rate range (a little lower for the lower MATe flux and little higher for the higher MATe flux).

5.3.2 Thickness and Composition Uniformity

Thickness and compositional uniformity maps of thin $Hg_{1-x}Cd_xTe$ epilayers on 20 cm² CdTe substrates, which were determined by MAI ellipsometric technique, are shown in Figure 5.16, for the system configurations of both single Hg inlet to the annular jacket and double opposing Hg inlets to the annular jackets. As can be seen in (a) of Figure 5.16, the compositional uniformity is excellent but the thickness uniformity reveals a gradient from 1101 Å to 1009 Å (\cong 10 % variation) when the single Hg inlet port configuration was employed. The composition of the thin $Hg_{1-x}Cd_xTe$ epitaxial layer obtained from the real part of refractive index from MAI ellipsometry data was *x*=0.92, which is not shown in Fig 5.14.

The highest growth rate of $Hg_{1-x}Cd_xTe$ epilayer was obtained on the side of the layer which was nearest the annular jacket inlet port and the lowest growth rate of $Hg_{1-x}Cd_xTe$ epilayer was obtained on the side of the layer farthest from the annular jacket inlet port. This result suggested that the Hg vapor was entering the reactor primarily from the side of the ARI opposite the annular jacket inlet port The second inlet port, therefore, was added to the annular jacket.

When the single Hg inlet port to ARIIV was modified to the double Hg inlet configuration, the thickness uniformity improved to 4 % variation, from 1008 Å to 970 Å over the 20 cm² substrate, but the compositional uniformity deteriorated somewhat to $x=0.602 \pm 0.002$, as shown in (b) of Figure 5.16. From a comparison



Figure 5.16 Thickness (Å) and compositional uniformity (refractive index) maps of thin $Hg_{1-x}Cd_xTe$ epilayer when (a) single Hg inlet and (b) dual Hg inlets were used. The refractive index of $Hg_{1-x}Cd_xTe$ in parenthesis.

of the single and double inlet configurations, even though two different growth recipes were used, it can be seen that the double Hg inlet ARIIV reactor improved the thickness uniformity but showed some variation in the compositional uniformity. However, it should be noted that the compositional uniformity was generally improved as the *x*-value increased, but it was difficult to find evidence that the thickness uniformity depended on the Cd concentrations as the growth of $Hg_{l-x}Cd_xTe$ epitaxial layers proceeded.

In order to determine if the excellent compositional uniformity of initial stage growth was maintained as growth proceeded, the compositional uniformity for a 4 µm thick $Hg_{1-x}Cd_xTe$ epilayer (x=0.152) grown on a 6.45 cm² CdTe substrate was measured by FTIR, as described in Section 3.4. The compositional profile for this $Hg_{0.848}Cd_{0.152}Te$ epitaxial single crystal showed excellent uniformity within the error $x = 0.152 \pm 0.002$, as presented in Figure 5.17.

This uniformity results using the ARIIV reactor are equal to or better than MBE uniformity [79] for substrate sizes of 2-4 cm and exceeds the uniformity achieved by the IMP growth technique [79], which has previously been considered to be superior in compositional uniformity to the DAG process. To our knowledge, this is the first time that state-of-the-art thickness and compositional uniformities have been obtained by DAG technique for $Hg_{l-x}Cd_xTe$ epitaxial layer by OMVPE.

5.3.3 Surface Morphology

The morphology of $Hg_{l-x}Cd_xTe$ epilayers (0.1 $\leq x \leq$ 0.4) grown at $T_s=300$ °C and 330 °C consisted of specular surfaces with a notable absence of ridges, which are usually observed when DIPTe or DETe is used as the Te reactant organometallic source [64, 76].



Figure 5.17 Compositional (x) uniformity of 4 μ m thick Hg_{1-x}Cd_xTe epitaxial layer over 6.45 cm² CdTe substrate.

The surface of $Hg_{l-x}Cd_xTe$ ($x \ge 0.6$) epitaxial layers grown at 330 °C contained hillocks, hillock densities in the range of 200 to 400 cm⁻², as shown in Figure 5.18 (a). This value is a order of magnitude lower than that obtained in HgTe epitaxial layers grown at 330 °C (normally 3000-5000 cm⁻²). At T_s=300 °C the surface of Hg_{1-x}Cd_xTe layers did not contain hillocks. This confirms that the improvement of morphology in Hg_{1-x}Cd_xTe epilayers grown is primarily due to the lower growth temperature.

When the flow rates of hydrogen through MATe and DMCd bubblers were doubled and the growth temperature was kept 330 °C, the hillock density was increased to 2000 cm⁻², as shown in Figure 5.18 (b). The increment of hillock density at higher flow rates of both MATe and DMCd is considered to be due to Cd-rich characteristics in OMVPE [79], because a doubling of MATe and DMCd increases the Cd concentration of the $Hg_{l-x}Cd_xTe$ layer due to the higher Cd partial pressure.

Specular surface was obtained in $\text{Hg}_{l-x}\text{Cd}_x\text{Te}$ ($x \le 0.4$) at 300 °C. Micro terraces, hillocks, or pits were not observed in any $\text{Hg}_{l-x}\text{Cd}_x\text{Te}$ (0.152 $\le x \le 0.356$) layers, Figure 5.19 (a).

Some elemental Hg splatters on the $Hg_{1-x}Cd_xTe$ grown layers were observed when the growth time was more than 3 hrs, as shown in Figure 5.19 (b). This was due to the condensed Hg in the primary inlet port, and the ARI port. This configuration problem of the ARIIV reactor tube can be eliminated by changing the primary inlet and the ARI port configuration, as shown in Figure 5.20.

The single crystallinity of $Hg_{l-x}Cd_xTe$ epilayers grown on CdTe substrates was confirmed by ECCP. One-fold bright bands about 1 mm wide were observed on the ECCP picture, confirming that the layer was monocrystalline, but the bright bands were not clear enough to be used for calculating composition of $Hg_{l-x}Cd_xTe$ crystal. An ECCP photo of a $Hg_{l-x}Cd_xTe$ epilayer is shown in Figure 5.21.







(b)

Figure 5.19 Nomarski contrast micrograph of $Hg_{l-x}Cd_xTe$ epitaxial layers showing (a) as-grown $Hg_{l-x}Cd_xTe$ and (b) Hg-splattered $Hg_{l-x}Cd_xTe$ surface.







Figure 5.21 Electron channeling contrast pattern of $Hg_{1-x}Cd_xTe$ epitaxial layer grown on CdTe substrate.

5.3.4 Defect Etching

A chemical etching technique was used to determine the distribution of crystallographic defects (dislocations, subgrain boundaries, or inclusions) in Hg₁₋ _xCd_xTe [75]. Shown in Figure 5.22 (a) and (b), are representative etch patterns of epitaxial layers of Hg_{1-x}Cd_xTe on (100) $5^{\circ} \rightarrow$ (110) CdTe substrates.

The morphology and the size of the pits with square shape are clearly observed. The etch pits are so small (~5µm) and have such uniform tops, that it is possible to observe etch pit densities up to $10^4 - 10^5$ cm⁻². The etch pit distribution was investigated after successive etching procedures, to determine whether the origin of subgrains in Hg_{1-x}Cd_xTe epilayers was due to grown-in dislocations or subgrain boundaries in the CdTe substrate. Subgrain boundaries of the CdTe substrate after several successive etchings are shown in Figure 5.22 (c). The result of the final etching confirmed that the origin of the subgrains in Hg_{1-x}Cd_xTe epilayers was due to subgrains result in lower mobilities and higher carrier concentrations (as discussed in the next section).

5.3.5 Electrical Properties of Hg_{1-x}Cd_xTe Epilayers

For *n*-type $Hg_{1-x}Cd_xTe$ samples, measurements at a few temperatures between 50 - 300 K can provide enough data for sample evaluation. *P*-type samples, on the other hand, require a detailed plot over this temperature range. Anomalous results often arise in higher x layers ($x \ge 0.25$) which are subject to surface inversion.

The effects of temperature on the Hall coefficient $R_{\rm H}$ for a representative asgrown *n*-type Hg_{1-x}Cd_xTe sample (x=0.126) is plotted in Figure 5.23. This coefficient is negative at low temperatures, because an excess of donors dominates extrinsic conduction; and remains negative in the intrinsic range at higher temperatures, because the electron mobility is greater than the hole mobility.

20 un (a) 0 (b) 10 µm (c)

Figure 5.22 Normarski contrast micrograph showing subgrain boundaries $Hg_{l-x}Cd_xTe$ (a,b) and subgrain boundaries CdTe substrate (c).



Figure 5.23 The Hall coefficient $R_{\rm H}$ as a function of reciprocal temperature for *n*-type Hg_{1-x}Cd_xTe (x=0.126) sample.

The variation of Hall mobility as a function of temperature for this Hg_{1-x}Cd_xTe (x=0.126) sample is shown in Figure 5.24. The rapid decrease of the electron mobility at temperatures above approximately 180 K is due to the dominance of lattice scattering mechanisms above this temperature. The slight fall in electron mobility at low temperature (≤ 70 K) can best be explained by taking ionized impurity scattering effects into account [20]. The effects of these two scattering mechanisms result in a maximum in the mobility characteristic at around 80 K. The mobility of this sample fell by only about 6% when the applied magnetic field was increased from 0.5 to 3 kG, thus confirming that the material is truly extrinsic. The basic carrier concentrations |N_a - N_d| were in the range (1.8 - 3.0) x 10¹⁶ cm⁻³, as shown in Figure 5.24.

The reason that as-grown *n*-type $Hg_{l-x}Cd_xTe$ layers with x=0.126 displayed a relatively constant carrier concentration and rather poor mobilities was determined to be subgrain boundaries in the CdTe substrates, as evidenced in Figure 5.22 (c) and from the 2 peaks in the DCXRC, Figure 5.6 (b).

The *p*-type $Hg_{l-x}Cd_xTe$ layers were obtained when the composition of asgrown materials was over *x*=0.25. The Hall coefficient and the hole mobility and carrier concentrations versus reciprocal temperature for a *p*-type $Hg_{l-x}Cd_xTe$ layer (x=0.266) is presented in Figures 5.25 and 5.26, respectively. The Hall coefficient is negative at higher temperatures because the material become intrinsic and the electron mobility is higher than the hole mobility. The change-over from negative to positive Hall coefficient takes place at about 130 K. The hole mobility is about 110 cm²/V. s with $|N_a - N_d| = 3 \times 10^{16}$ cm-3. No clear extrinsic region in the Hall coefficient is seen, probably because the extrinsic region and the region at which *R*_H reverses sign overlap. The mechanism which accounts for the anomalous behavior of *p*-type $Hg_{l-x}Cd_xTe$ materials was explained in detail in Chapter 2.



Figure 5.24 The Hall mobility and the Hall carrier concentration as a function of reciprocal temperature for *n*-type $Hg_{1-x}Cd_xTe$ (x=0.126) sample.



Figure 5.25 The Hall coefficient $R_{\rm H}$ as a function of reciprocal temperature for *p*-type $Hg_{1-x}Cd_xTe(x=0.266)$ sample.

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Figure 5.26 The Hall mobility and the Hall carrier concentration as a function of reciprocal temperature for *p*-type $Hg_{l-x}Cd_xTe$ (x=0.266) sample.

The electrical data on $Hg_{l-x}Cd_xTe$ grown in the ARIIV reactor OMVPE system are summarized in Table 5.1. These data indicate that the n- or p-type behavior of $Hg_{l-x}Cd_xTe$ layers is attributable to a number of factors: 1) the use of a low growth temperature of $Hg_{l-x}Cd_xTe$ crystals results in a low concentration of Hg vacancies, which are acceptors, and 2) the lattice mismatch and subgrains in the CdTe substrate result in defects that are active and probably *n*-type, as evidenced by the low value of mobility and the higher carrier concentrations.

sample #	Q _{H2-DMCd} (sccm)	composition (x)	thickness (µm)	type	mobilities (cm ² /V.s)	carr. cont. (cm-3)
mct-ct35	0.6	0.126	5	n	1.8 x 10 ⁴ (300 K) 2.7 x 10 ⁴ (77 K)	2.5 x 10 ¹⁶ (300 K) 1.7 x 10 ¹⁶ (77 K)
mct-ct47	0.6	0.352	4	n	?	7.0 x 10 ¹⁶ (300 K)
mct-ct46	0.9	0.152	4	n	2.0 x 10 ⁴ (77 K)	4.0 x 10 ¹⁵ (77 K)
mct-ct36	1.2	0.126	4	n	2.5 x 10 ⁴ (300 K) 4.1 x 10 ⁴ (77 K)	6.2 x 10 ¹⁶ (300 K) 2.6 x 10 ¹⁶ (77 K)
mct-ct37	1.2	0.266	3	р	1.8 x 10 ³ (300 K) 1.5 x 10 ² (77 K)	2.6 x 10 ¹⁵ (300 K) 2.7 x 10 ¹⁶ (77 K)
mct-ct45	1.8	?	4		?	?
mct-ct41	2.4	0.288	1.2	р	1.4 x 10 ³ (300 K)	2.3 x 10 ¹⁶ (300 K)
mct-ct43	3.6	0.297	0.9	р	2.1 x 10 ³ (300 K)	1.7 x 10 ¹⁶ (300 K)

Table 5.1 Electrical data of $Hg_{l-x}Cd_xTe$ grown by cold wall, pyrolytic OMVPE.

6. CONCLUSIONS

With a new ARIIV reactor a breakthrough in the composition and the thickness uniformities of $Hg_{l-x}Cd_xTe$ epilayers by the DAG process OMVPE was achieved for the first time.

The epilayer growth performance of the ARIIV reactor using DMCd, MATe, and elemental Hg as OMVPE reactant sources was characterized by growth rate and surface morphology studies for CdTe and HgTe epilayers.

Optimum flow rates, substrate temperature, and reactor conditions were experimentally determined for $Hg_{1-x}Cd_xTe$ epilayer growth at 300 °C; where, T_s was determined by the cracking properties of MATe alkyl. The thickness and compositional uniformities of 16 cm² $Hg_{1-x}Cd_xTe$ epilayers were better than ±2 % and ±0.002 in x, respectively.

The Hall effect measurements on as-grown $Hg_{l-x}Cd_xTe$ showed *n*-type characteristics for less than x=0.25 and *p*-type characteristics for greater than x=0.25. The electron carrier concentrations and the electron mobilities for *n*-type $Hg_{l-x}Cd_xTe$ materials were in the range (0.4 - 2.6) x 10¹⁶ cm⁻³ and (2.0 - 4.1) x 10⁴ cm²/V.s, respectively, at 77 K. The higher value of electron concentrations and the lower value of electron mobilities for n-type $Hg_{l-x}Cd_xTe$ were due to the subgrain boundaries in the available CdTe substrates.

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