PREDICATE DRIVEN MODEL OF DATA FLOW COMPUTATION

Ashoke Deb*

Oregon Graduate Center 19600 N.W. Walker Rd. Beaverton, OR 97006

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Abstract**

The concept of data flow computation has several attributes and provides interesting solutions to problems encountered in von Neumann style of computation, namely simple synchronization mechanism, side effect free programming, increased exposure to parallelism and asynchronism. But, the conventional tools for representing a data flow computation is either based on imperativestyle program structures or a very low level graph model, usually obtained from an imperative-style program. This makes the understanding and exploitation of parallelism difficult and also the underlying architecture becomes highly complex.

We propose a model, called Predicate Driven Model (PDM), for representing data flow computations. This model has several advantages:

- i) the types of constructs, namely <u>Function-node</u>, <u>Input initialization</u> and Line composition, are minimal and simple,
- ii) two synchronization tools, one by 'encapsulation' and the other by 'arrival', are available and implicit in the model,
- iii) transformation to a graph notation is direct, and
- iv) the elimination of imperative structures, exposes 'hidden' parallelism much more effectively than the traditional approaches.

KEY WORDS: Data flow computation, von Neumann system, Imperative programming, Functional programming, Single-assignment, Parallelism, Synchronization.

^{*} On leave from Memorial University; St. John's, Newfoundland.

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I. Background

A growing body of computer architects, language designers and software developers believe that the traditional ideas built around von Neumann model of computation should be abandoned.

From the architectural viewpoint, von Neumann designs have the following characteristics. (1) It distinguishes control elements from data operational elements. (2) Space (e.g., variable locations), as opposed to values, are the primary objects. (3) As a result of (1) and (2), control initiates operation whose domain and range are spaces. An operation modifies a space. This modified space is responsible for the next control signal. Thus, control-operationspace model works entirely on side effect (3). Even our notion of categorizing systems as (instruction stream, data stream) pair is an implicit way of classifying control-operation-space model.

From the language designer's viewpoint, the influence of von Neumann characteristics are obvious. (1) The most basic element in a language is a variable, which sometimes represents a value and sometimes a location, depending upon its context. (2) Most such languages are endowed with features facilitating side effects e.g., common block, call-by-reference, call-by-name, assignment statements of the form A := A + B, to name a few. (3) Transfer of control features e.g., GOTO, procedure CALL, conditional branch, came into existence in order to optimize the use of operations and spaces in that control-operation-space model.

Over the number of years, software developers were trained to be 'clever' users of side effects and control transfers. Sometimes higher level constructions were developed to hide the low level complications, but understanding and implementation of these high level constructs depended on extensive (and often complex) use of side effects and control transfer mechanisms. For example, (1) implementation of P, V operations on semaphores or other variations of it depend exclusively on side effects produced on the semaphore variable. (2) After a decade of realizing the danger of GOTO or its equivalent, these constructs are still well and alive. Mathematicians try to prove the correctness of a program with great difficulty and in a roundabout way. For example, it is difficult to mathematically justify A = A + B. Denotational semantics of GOTO is not easily given. Also, programs written using conventional constructs such as IF-THEN-ELSE, FOR-DO, Assignment etc. do not expose parallelly computable parts and unravelling of such structures is hard due to intricate 'data dependencies'.

Hence realized was the need of (1) computer architectures [2,4,6,8,10,14] that can free itself from the von Neumann design bottlenecks. (2) A language [1,2,3,9,13] that can be used to express parallelism within a computation, such that it is mathematically sound, free of side effects, free of complex control mechanics and also which is readable and descriptive of a computation.

At present, data flow computation [1,2,4-8,11,14] seems to provide a viable alternative to von Neumann computation. In data flow, 'values' as opposed to variable locations are considered as the primary objects. Operations are performed on operand values to produce result values. The usual interpretation of a data flow computation is that the computation is described as a network such that values flow (operand values arrive at the operation node and resultant values flow out of the operation node) through the network, finally producing the result. To illustrate the difference between von Neumann computation and data flow computation, we show the following example.

Example:

To evaluate the following function

f(n) = n * (n - 1) * (n - 2)

von Neumann style of computation will be as follows:

LOAD N SUB ONE STORE TEMP SUB ONE MULT TEMP MULT N

N	/* The value of n */
ONE	/* The constant l */
TEMP	/* A temporary variable location */
AC	/* An implicit variable location */
	used by an operatoin as accumulator */

On the other hand, data flow style of computation can be expressed as follows:



Points of difference between these two styles of computation are that in the first method (1) variable locations are key objects to be manipulated, (2) side effects (e.g., AC = AC * TEMP and AC = AC * N etc.) are used to minimize the code length (hence execution time) and the storage space, (3) as a result of that, parallel computations are obscured, whereas in the second method (1') values are the key objects and functions are applied to produce result values, (2') an operation is activated by the arrival of valid input values, (3') there are no side effects and (4') parallelism of computations can be directly expressed and computations can proceed asynchronously.

End of Example.

Recently, functional (applicative) languages [1,2,7,9,13] have received a good deal of attention because (1) they do not use any side effects, (2) these were designed (in purest form) to manipulate values rather than variables and (3) being functional, mathematical theorems can be used more easily for reasoning.

To illustrate the difference between von Neumann (imperative) style and functional (applicative) style of programming, we show the following example.

Example:

To find the sum of the squares of the numbers from 1 to n.

In an imperative style this might be written as:

sum := 0; FOR i := 1 to n DO sum := sum + square (i);

Above code does not need any explanation to most 'experienced' programmers, because they 'know' the meanings of 'sum := sum + square (i)' and 'FOR i := 1 to n DO' etc.

In applicative style one might write:

Listadd o Elementwise-square o Allnumbers [1,n]

The applicative program is shorter, free of intermediate variables and more meaningful to one who is not corrupted by imperative style of thinking. (Read the above program as "Add up elementwise square of all numbers from 1 to n".) In this, 'Listadd', "Elementwise-square', 'Allnumbers' are functions. 'o' is the function composition operator. A function application is right associative.

End of Example.

Currently, one of the major issues in the design of functional languages is to provide a set of functions and functional operators which can be used to construct other functions.

Aside from the mathematical elegance of functional languages, the current approaches do not provide enough 'granularity' to allow the programmer to dictate the flow of data. For example, in the last functional style program, if the programmer wants to modify the program so that computation terminates as soon as square (i) > BIGNUMBER, for some i, $1 \le i \le n$, and a constant BIGNUMBER, modification becomes implementation dependent and not so immediate. Also, once a function is defined, the domain and the range associated with the function is fixed. A simple way of imposing property on the domain (or the range) and associating this directly with a function definition is desirable, which seems to be a common usage in mathematics.

For example, given a definition of * (multiply), define F_2 such that $F_2(x,y) = r = x * y$ such that $r \neq 16$, i.e., F_2 'produces' results of product of two numbers such that the result is not sixteen. In other words, the range of F_2 is a restriction of the range of *.

In the context of data flow computation, the direct association of properties (i.e., restrictions) on both the domain (i.e., input values) and the range (i.e., output values) of a function has important implications which will be discussed later in the paper.

Traditionally, a data flow computation is represented as a program by using a 'data flow language', such as ID or VAL [1,2], or by a data flow program graph, sometimes referred to as the base language of the data flow machine [5,6, 12].

In the following, we will discuss why the above tools are not adequate for representing data flow computations.

Data flow languages (e.g., VAL) allow structures such as IF-THEN-ELSE, WHILE-DO, Assignment, etc. for writing programs imperative style.

We will show an elementary data flow program written in VAL and its data flow graph.

Example:

(Assume that x and w are some given values.)

End of Example.

Another commonly used tool for representing data flow computations is data flow graph whose constructs are based on 'links' and 'actors' and their 'firing rules'. In one such model [6], the links, actors and firing rules are as follows [Fig. 2-5]. Figure 6 shows the data flow graph of the example program written in VAL. 4





Figure 2: Node types for data flow program.

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Figure 3. Firing rules for link nodes.







T-gate



merge



Figure 5. Firing rules for control actors.



Figure 6: An elementary data flow program.

It should be noted that in VAL, expression of the form 'iter y := y/2' is different from the imperative form 'y := y/2' (as a matter of fact, VAL does not allow such assignments). 'iter y := y/2' like expressions can be used only inside a loop and essentially means that the 'value of y in the next iteration' is equal to the 'value of y in the current iteration' divided by 2.

Still a closer look at languages of this type and their semantic models reveal several drawbacks.

- (1) Use of traditional constructs (or their modifications) such as IF-THEN-ELSE, WHILE-DO, etc. which do not lend themselves to clear specification of parallelly computable parts. In the last example, the fact that 'iter y := y/2', 'iter y := y/3' and 'iter t := t + 1' are parallel computations is not revealed. This is also the case in the graphical view of the computation.
- (2) Although expressions of the form y := y/2 is not allowed and the 'principle of single assignment' is used, the fundamental idea, that the value of y from one cycle of computation is fedback (in true sense), is kept implicit. Notice that in von Neumann style of computation where controlling the instruction sequencing was an important issue, programmers were given facilities to accomplish 'flow of control'. On a similar basis, in data flow style of computation, programmers should be given facilities to directly specify the 'flow of data values'.
- (3) Finally, we need a higher level semantic model, which will express functionality, parallelism and synchronization in a structured form.

In the next section, we present a model, called Predicate Driven Model (PDM), of data flow computation. Some of the important characteristics of the PDM are that (1) there are three main constructs which are used to represent a data flow computation, and (2) the semantics of these constructs rely on two levels of synchronization--by containment and by arrival, on queuing of the input values and on 'local' data validation at the input and output lines of a node. There are two other important features a special value λ and 'memory less' property of the output lines, which will be discussed later.

In the last section, we will discuss our future goals and areas of research.

II. Predicate Driven Model of Data Flow Computation

In PDM, a computation is a composition of functions, where:

- i) Function-body is a definition of input to output mapping.
- ii) Domain is a definition of all valid input values of a function-body.
- iii) Range is a definition of all valid output values of a function-body.
- iv) Activation of a function body takes place only if
 - a) previous activation of the function-body has completed
 - b) a set of all valid input values in the domain of the function has arrived.

Note that the input (output) values arriving at (leaving from) the function-body may not all be valid. Activation of a function-body does not necessarily require that output value(s) from its previous activation be consumed. Also, it is possible to have more than one function with identical functionbody but different domains and/or ranges. Implications of these will shortly be explained.

Structure of a computation may thus be looked upon as a synthesis (or refinements) of several structures such that the range (or subrange) of one is a subset of the domain of another [Figure 7].



Figure 7: Structured representation of a computation, $D{F}R$, where F is the function-body, D is the domain, R is the range, which is a collection of lower level of computations $D_i {F_i} R_i$. $1 \le i \le n$ such that $R_i \subseteq D_k$ for some j, k's. For the sake of subsequent discussions, we will make the following assumptions:

- i) V is a set of values, $V = \{v_1, v_2, ..., v_N\}$, called the Universe of Discourse, which includes a special value λ . The highest level domain and range are subsets of V, e.g., $V = \{integers, \lambda\}$. The meaning and use of λ will be explained later.
- ii) OP is a set of primitive operators, OP = $\{0_1, 0_2, \ldots, 0_n\}$, such that

$$D_i: v \to v$$
,

for some integers n and m and v \in V, e.g., OP = {+, -, /, *, Div, Mod, <, =, >} .

The PDM uses three basic constructs, namely <u>Function Node</u>, <u>Line Composition</u> and Input Initialization, to represent a computation.

Function Node:

Syntax of a function node f, without input initialization, is as follows:

where
$$D = [C_1] I_1 [C_2] I_2 \dots [C_n] I_n$$
,
 $R = [C_1'] O_1 [C_2'] O_2 \dots [C_m'] O_m$,
 $F = f : (I_1, I_2, \dots, I_n) \rightarrow (O_1, O_2, \dots, O_m)$

In the above,

f: $D{F}R$

f is the <u>function name</u>, which is optional, $\begin{bmatrix} C_1 \begin{bmatrix} I_1 & C_2 \end{bmatrix} I_2 & \dots & \begin{bmatrix} C_n \end{bmatrix} I_n$ is the list of <u>predicated input lines</u>, I_1, I_2, \dots, I_n are symbolic names, called <u>input lines</u>, C_1, C_2, \dots, C_n are predicates, called <u>input predicates</u> such that C_1 is formed by using logical and relational operators from OP, constants from V and I_1, I_2, \dots, I_n such that any occurrence of I_1 is free.

Similarly,

[C₁'] O₁ [C₂'] O₂ ... [C_m'] O_m is the list of predicated output lines, O₁, O₂, ..., O_m are symbolic names, called output lines, C₁', C₂', ..., C_m' are predicates, called output predicates, such that each C₁' is formed by using logical and relational operators

from OP, constants from V and $I_1, I_2, \ldots, I_n, O_1, O_2, \ldots, O_m$ such that any occurrence of I_k or O_1 is free.

Lastly,

{f: $(I_1, I_2, ..., I_n) \rightarrow (0_1, 0_2, ..., 0_m)$ } is called the <u>function body</u> which consists of zero or many mappings of the form $E_j \rightarrow 0_i$. E_j is an expression formed by using operators from OP, constants and V and $I_1, I_2, ..., I_n$. Also there does not exist two mappings in a function body such that $E_j \rightarrow 0_i$ and $E_k \rightarrow 0_i$ for $j \neq k$.

The semantics of a function node f, without input initialization, is as follows:

Let v be the value present in input line I. (output line O.) at an instant. v is said to be valid if the corresponding input predicate C. (output predicate C.') is true. If an input or output predicate is the constant TRUE, then it is usually omitted.

A function node is said to be firable if (i) the activity due to the last firing is complete and (ii) a set of valid input values are available in all input lines.

If a value v is present at the input line I such that v is not valid, then v is called invalid input and its effect is equivalent to the removal of the value v from line I.

Note: Since each input line validates its own value, it is possible to allow two input lines, I_1 and I_2 , with predicates C_1 and NOT (C_1) respectively and the input value v to both I_1 , I_2 . This may provide for two distinct actions for two classes of input values to be taken. An example of this is where 'incorrect' values are used to produce error messages. End of Note.

If a value v is obtained for output line 0 such that v is not valid, then the special value λ is produced at line 0.

An input line I is allowed to maintain a sequence of values.

An output line O is not allowed to retain its value.

Firing of a function node is said to be <u>complete</u> when the entire function body has been executed, values have been produced at the output lines and the present input values have been removed from the input lines.

<u>Note</u>: According to the above requirements of the firing of a node, two levels of synchronization are present. One is achieved by the arrival of all valid input values. The other is achieved by the encapsulation of several operations of the form $E_j \rightarrow 0_i$ within the function body. All such operations within a function body may proceed in parallel.

Also, a sequence of output values may be retained by 'channelling' those values to an input line. End of Note. In the following, we will present and discuss a few examples.

Example: Let

 $\begin{array}{l} f_{1} : [15 \geq I_{1} \geq 10] \ I_{1}[I_{2} = 5 \ \text{OR} \ I_{2} = 6] \ I_{2} \ \{I_{1} \, \ast \, I_{2} \rightarrow P\} \ [] \ P \\ \end{array} . \\ \\ This is equivalent to the imperative style statement \\ IF ((15 \geq I_{1} \geq 10) \ \text{and} \ ((I_{2} = 5) \ \text{OR} \ (I_{2} = 6))) \\ \\ THEN \ P := I_{1} \, \ast \, I_{2} \ . \\ \\ \\ Also, let \end{array}$

 $f_2 : [] I_1 [] I_2 \{I_1 * I_2 \rightarrow P\} [15 > I_1 > 10 \text{ AND } (I_2 = 5 \text{ OR } I_2 = 6)] P$

Although f_1 and f_2 appear similar, there are a number of important differences between them. In f_1 , the function body will be executed for only a subset of integer for which the predicates $(15 > I_1 > 10)$ and $(I_2 = 5 \text{ OR } I_2 = 6)$ are true. On the other hand, in f_2 there are no input predicates associated with it; the function-body of f_2 will be executed for all values of I_1 and I_2 . Secondly, f_1 will never produce λ output, whereas f_2 may produce λ output (when the output predicate is false). Thirdly, in terms of actual execution sequence, in f_1 , the evaluation of the input predicates and the execution of the function body is strictly serial. Whereas in f_2 , the execution of the function body and the evaluation of the output predicate may proceed in parallel.

End of Example.

End of Example.

Example: Let

$$f : [I_1 > 5] I_1 \begin{cases} square (I_1) \rightarrow P; \\ maxint \rightarrow Q \end{cases} \qquad [P > maxint] P [P \ge maxint]Q$$

where 'square' is an element of OP and 'maxint' is an element of V.

A logically equivalent imperative style statement would be:

```
IF (I<sub>1</sub> > 5) THEN

BEGIN

P := square (I<sub>1</sub>);

Q := maxint;

IF (P > Q) THEN Q := \lambda ELSE P := \lambda

END.
```

1

Another type of construct used in PDM, called <u>Line Composition</u>, is defined as follows.

Line Composition

The syntax of a line composition is as follows:

I + 0

where O and I are output and input lines, respectively.

A set of line compositions of the form $I_1 + 0$, $I_2 + 0$, $I_3 + 0$, ..., $I_p + 0$, called Fan-out Line Composition, is equivalently expressed as

 $I_1, I_2, I_3, \ldots, I_p \neq 0$.

A set of line compositions of the form $I + 0_1$, $I + 0_2$, $I + 0_3$, ..., $I + 0_k$, called <u>Fan-in Line Composition</u>, is equivalently expressed as

$$I \neq 0_1, 0_2, 0_3, \dots, 0_k$$

The semantics of line composition are as follows.

The meaning of $I_i + O_j$ is that the value generated at O_j will arrive at I_i . The meaning of a fan-out line composition $I_1, I_2, \ldots, I_p + O$ is that the value generated at O will arrive at each of I_i , for all i, $1 \le i \le p$.

The meaning of a fan-in line composition $I \leftarrow 0_1, 0_2, \ldots, 0_k$ is that the value generated at each 0, will arrive (asynchronously) at I. The values arriving at I will be <u>serialized</u> according to their order of arrival. In case more than one value arrives at I at the same time, then they will be serialized arbitrarily.

In case the input line I has an input initialization sequence, the serialization of additional incoming values will take place following the initial sequence of values. (Input initialization is described later.) In the following, we will present a few examples.

PDM provides a powerful tool for composing (connecting) function nodes. Here we will illustrate three type of compositions, called <u>Inside fit</u>, <u>Outside</u> fit, and <u>Partial fit</u>. [See Figure 8]



Figure 8. Inside fit

Outside fit

```
Partial fit
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Let $0 = \text{set of output values flowing from } f_1 \text{ to } f_2, \text{ and}$ $D = \text{domain of } f_2.$ We call the composition of $f_1, f_2,$ an inside fit, if $0 \cap D = 0$ an outside fit, if $0 \cap D = D$ and a partial fit, if $0 \cap D \neq D$ or 0.

A composition which is both an inside fit and an outside fit is called a total fit.

Example (Outside fit)

Let

$$f_{1} : [] I_{1} \{f_{1}(I_{1}) \rightarrow 0_{1}\} [5 \leq 0_{1} \leq 10] 0_{1} ;$$

$$f_{2} : [I_{2} = \lambda] I_{2} \{f_{2}(I_{2}) \rightarrow 0_{2}\} [] 0_{2} ;$$

$$I_{2} \neq 0_{1} .$$

In this example, what f_2 does is take an 'action' (denoted by the function f_2) when f_1 produces invalid outputs.

```
End of Example
```

```
Example (Partial fit)
Let the function f<sub>1</sub>(n) compute the nth element of the Fibonacci sequence
and f<sub>2</sub>(x) compute 'x modulo 10.
    f<sub>1</sub> : [n > 0] n {Fib (n) \rightarrow 0_1} [] 0<sub>1</sub>;
    f<sub>2</sub> : [even x] x {x mod 10 \rightarrow 0_2} [] 0<sub>2</sub>;
        x + 0<sub>1</sub>.
The predicate even x is true if x is even.
```

End of Example

The following examples show iterative composition on a function.

Example (Indeterminate loop)

Let

f : [C] I {f(I) \rightarrow 0} [] 0 ; I \leftarrow 0 .

Here at termination, $0 = f^n(I)$ such that $n \ge 0$, C is true for all i < n, when $0 = f^i(I)$ and C is false when $0 = f^n(I)$.

End of Example

Example (Determinate Loop)

Let

$$f: [] I [N \neq 0] N \begin{cases} f(I) \rightarrow 0_1; \\ N-1 \rightarrow 0_2 \end{cases} [] 0_1 [] 0_2 ;$$

$$I \leftarrow 0_1 ; N \leftarrow 0_2 .$$
Here at termination, $0_1 = f^N (I) .$

End of Example

Example (Determinate loop with exit condition)

Let

$$f: [C_1] I [N \neq 0] N \begin{cases} f(I) \rightarrow 0_1 \\ N-1 \rightarrow 0_2 \end{cases} [] 0_1 [] 0_2 ;$$

$$I + 0_1 ; N + 0_2 .$$
Here at termination. $0_1 = f^n (I)$ such that $0 \le n \le N$

Here at termination, $O_1 = f''(I)$ such that $0 \le n \le N$ and for all $i \le n$, $O_1 = f^i(I)$ and C_1 is true.

End of Example

Input initialization

Input initialization provides the facility in a function node definition where an input line may have a sequence of initial values.

The syntax of such construct is as follows.

I. init $(v_1, v_2, ..., v_p)$ where I is the symbolic name of an input line to a function node, init is a key word, $v_1, v_2, ..., v_p$ are elements of V.

The semantics of such construct is as follows.

For the first p instances, the sequence of values arriving at the input line I will be v_1, v_2, \ldots, v_p , in that order.

If additional values arrive at I (as a result of line composition) before its initial values v_1, v_2, \ldots, v_p are consumed, the additional values will be serialized following v_p .

Example (Selective mapping)

Suppose we have a sequence of values $(v_0, v_1, ..., v_n)$ and a sequence of binary selection values $(b_0, b_1, ..., b_n)$ such that $f(v_1)$ is computed if $b_1 = 1$.

f : X Y {f(I) \rightarrow 0} [] 0 where X is [B \neq 0] B.init (b₀, b₁, ..., b_n) and Y is [B \neq 0] I.init (v₀, v₁, ..., v_n).

End of Example

It is well known that asynchronism is used to achieve speed up. But the nondeterministic nature of it have led some to incorrectly assume that such computations are inherently difficult to express. The following example is presented to show that it is not the case. Simpler, terser and more transparent representation of a computation, which exploits asynchronism, than any imperative style of programming is demonstrated.

Example

Let O_1, O_2, \ldots, O_n be the results produced by n independent computations and, hence, the arrival time of these results may be different. We want to compute the cumulative sum of these results. The following achieves this goal.

f: []I [] S.init (0) {I + S \rightarrow 0} []0 ; I + 0₁, 0₂, ..., 0_n.

A comparative analysis of the above with an imperative style program is useful.

The following code

 $S := 0_{i};$ FOR k := 1 TO n DO $S := S + O_{k};$

will add $0_1, 0_2, \ldots, 0_k$ exactly in that order but not as they are available.

End of Example

Before we conclude this section, we will present solutions of some common problems, as expressed in PDM, including the one illustrated in Section 1.

Example The following expresses the computation as expressed in VAL and the . 'dataflow' diagram in Section 1.

 $f: [y \neq \lambda] \text{ y.init } (x) [t \neq w] \text{ t.init } (0) \begin{cases} y/2 \neq 0_1 \\ y \neq 3 \neq 0_2 \\ t+1 \neq 0_3 \end{cases} [y > 1] 0_1 [y \ge 1] 0_2 []0_3 ;$ $t \neq 0_3 ; y \neq 0_1, 0_2 .$

End of Example

<u>Example</u> Computing Factorial (n), n > 0. $f_0 : [I > 0]$ I.init (n) $\{I - 1 \rightarrow 0_1\} []0_1$; $f_1 : []$ Y.init (1) [x > 0] X.init (n) $\{X * Y \rightarrow 0_2\} []0_2$; $X, I + 0_1$; $Y + 0_2$

End of Example

Example Recognizing a string of matched and well balanced left- and rightparentheses. Assume that the string is given by v_0, v_1, \ldots, v_n where $v_i = (or)$ $0 \le i \le n$ and $v_n = \$$, end of sequence marker.

$$f: [] I_1.init (v_0) [I_2 \neq \lambda] I_2.init (v_1, ..., v_n) \begin{cases} I_1 \neq 0_2 \\ I_2 \neq 0_1 \end{cases} [NOT C] O_1[NOT C] O_2$$
$$I_1 \neq O_1; I_2 \neq O_2 ,$$

where the predicate C is $I_1 = (AND I_2 =)$, and NOT is logical negation. End of Example

It should be pointed out that the algorithms used here are for illustration purposes only and are not meant to be "the" solution.

Example Job Dispatcher.

Previously, we studied problems where data is <u>deterministically sent</u> to a definite processor. In those examples, it is the arrival of data that starts a processor--i.e., a valid data <u>arrives</u> at a processor and waits until the processor becomes 'free'. Now suppose that there are M independent processors, all of which are capable of processing a data -- in other words, as far as a data is concerned, all processors are identical.

We also have N data items, which we would refer to as jobs.

The problem is to process N jobs on M processors as rapidly as possible.

Obviously, a deterministic passing of a job to a processor does not solve the above problem due to possible processor bottlenecks.

The solution also must incorporate the following facts that:

- (1) if there is an available processor and there is an unprocessed job, the unprocessed job must be dispatched to an available processor.
- (2) if there is more than one available processor, then <u>exactly</u> one should process the job,
- (3) use minimum number of interprocessor communications and
- (4) lastly and most important, the solution must not use any side effects.

In the following, we present such a solution expressed in PDM.

It should be noted that the dispatcher and the processors are assumed to be cyclic and the dispatcher at the beginning of its life will have the numbers of all the processors.

Dispatcher: [] AvailableProcessorNO [] Job.init (Job₁, Job₂, ..., Job_N) {AvailableProcessNO $\rightarrow 0_1$, Job $\rightarrow 0_1$ } []0, []0, ; $I_1, I_2, \ldots, I_M + O_1$; $JobR_1$, $JobR_2$, ..., $JobR_M \leftarrow O_2$; Processor k: $[I_k = k]I_k$ []JobR_k ${Process (JobR_k) \rightarrow 0_{1k}}$ 1 < k < M $k \rightarrow 0_{2k}$ []0_{1k} []0_{2k} ; AvailableProcessNO $\leftarrow 0_{2k}$.

End of Example

III. DISCUSSIONS AND FUTURE GOALS

Earlier in the paper, we have mentioned that directly associating predicates on both the input and the output ends of a function-node have important implications in the context of data flow computations.

Some of the advantages are that:

(1) the representation of the computation becomes concise, because an additional function composition can be avoided. Ε

.g.,
$$f_1 : [C_1] I \{f(I) \rightarrow 0\} [C_2] 0$$

is equivalent to

 $f_0 : [C_1] I_1 \{f(I_1) \rightarrow 0_1\} [] 0_1 ;$ $f_1 : [C_2] I_2 \{I_2 \rightarrow O_2\} [] O_2 ;$ $I_2 + 0_1$

(2) properties (of values) and their evaluations are kept 'local', thus avoiding 'superfluous' passing of values to another function node. E.g., suppose the predicate C in the following example, depends on inputs I_1 , I_2 and the output O_1 . r 1

$$f : [] I_1 [] I_2 \{f(I_1, I_2) \rightarrow 0_1\} [C] 0_1 ;$$

this would be equivalent to

$$f_{0} : [] I_{3} [] I_{4} \{f(I_{3}, I_{4}) \rightarrow 0\} [] 0 ;$$

$$f_{1} : [] I_{5} [] I_{6} [C] I_{7} \{I_{7} \rightarrow 0_{1}\} [] 0_{1}$$

$$I_{3}, I_{5} + I_{1} ; I_{4}, I_{6} + I_{2} ; I_{7} + 0$$

 (3) also, by placing predicates at the output end of the function node, parallelism may be exposed and exploited to a greater extend. E.g., suppose the predicate C in the following example depends on the input I.

In f: [C] I $\{f(I) \rightarrow 0\}$ [] 0, the predicate C has to be evaluated and if successful, then computation of the function-body may proceed. This may be necessary for some computation. But, there are computations where the predicate C is used only for selecting or not selecting a result of computation. In those cases, evaluation of C and the function-body could be done in parallel. That can be done in f: [] I $\{f(I) \rightarrow 0\}$ [C] 0.

At present, we have implemented an interpreter on VAX 11/780 under Unix operating system for experimental purposes.

So far, we have not addressed the issues related to dynamic data flow systems. We are currently working on several ideas which, we believe, will nicely extend the present level of PDM to dynamic systems.

REFERENCES

- Ackermann, W. B. and J. B. Dennis [1979], "VAL A value-oriented algorithmic language -- Preliminary Reference Manual", MIT/LCS/TR-218, June 1979, 80 pp.
- Arvind, K. P. Gostelow and W. E. Plouffe [1978], "An asynchronous programming language and computing machine", TR 114a, Dept. of Info. and Computer Science, Univ. of California - Irvine, December 1978.
- 3. Backus, J. [1978], "Can programming be liberated from the von-Neumann style? A functional style and its algebra of programs", CACM, 21, 8, August 1978, pp. 613-641.
- Comte, D., N. Hifdi and J. C. Syre [1980], "The LAU data driven multiprocessor system: results and perspectives", Proc. IFIP '80, Tokyo and Melbourne, October 1980, pp. 175-180.
- 5. Dennis, J. B. [1971], "On the design and specification of a common base language", Proc. of Symp. on Computers and Automata, Polytech. Press, Brooklyn, NY, 1971.
- 6. Dennis, J. B. [1980], "Data Flow Supercomputers", IEEE, Computer, November 1980, pp. 48-56.
- 7. Glauert, J. [1978], "A single assignment language for data flow computing", MSc Thesis, Dept. of Computer Science, Univ. of Manchester, January 1978.
- Gurd, J. and J. Watson [1980], "A data driven system for high speed parallel computing", Computer Design, 19, 6-7, June/July 1980, pp. 91-100/97-106.
- 9. Henderson, P. [1980], <u>Functional Programming -- Application and Imple-</u> mentation, Prentice Hall, 1980.
- Magó, G. A. [1979], "A network of microprocessors to execute reduction languages", Int. JCSS, 8, 5 & 6, October-December 1979, pp. 349-385/435-471.

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- 11. McGraw, J.R. [1980], "Data flow computing -- software development", IEEETC, C-29, 12, December 1980, pp. 1095-1103.
- 12. Rodriguez, J.E. [1969], "A graph model for parallel computation", MAC-TR-64, Project MAC, MIT, Cambridge, Mass., September 1969.
- 13. Proc. of the 1981 Conference on Functional Programming Languages and Computer Architecture, ACM #556810, October 1981.
- 14. Special Issue on Data Flow Computers, IEEE, Computer, February 1982.